

CMPT 745
Software Engineering

What are programs?

Nick Sumner
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 - Security!
- We cannot reason about programs in only one way

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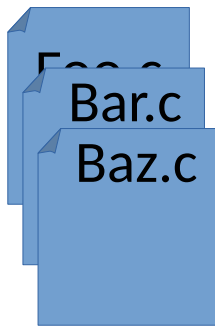
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Why might binaries be good for security tasks?

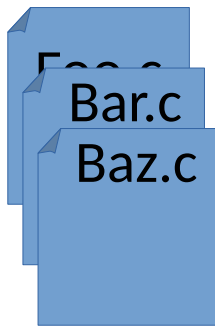
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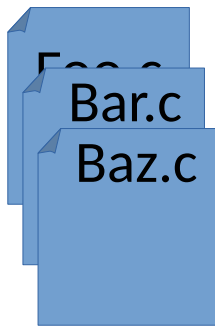
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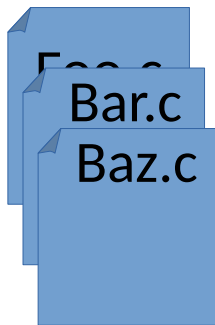
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- **Difficult models:**
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 - **Source code**
 - Very language specific
 - Relationships can be hard to extract
 - Often used when relating to comments or specs



Program Representation

- Before we can reason about programs, we must have a vocabulary and a *model* to analyze
- Difficult models:
 - Compiled binaries
 - Source code
- A *good* representation should make explicit the relationships you want to analyze

Program Representation

Core graph representations for analysis:

- 1) Abstract Syntax Trees
- 2) Control Flow Graphs
- 3) Program Dependence Graphs
- 4) Call Graphs
- 5) Points-to Graphs
- 6) Emerging Representations for ML

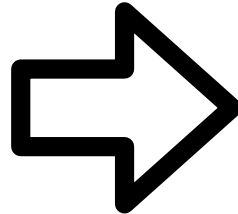
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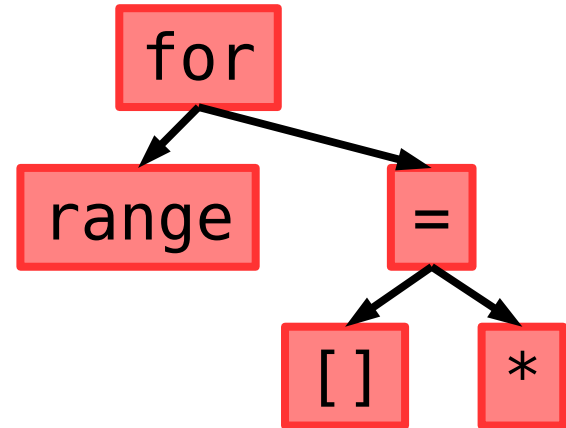
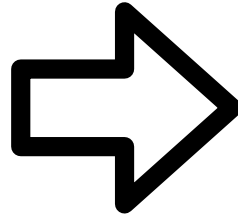
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 - **Internal** nodes are operators, statements, etc.

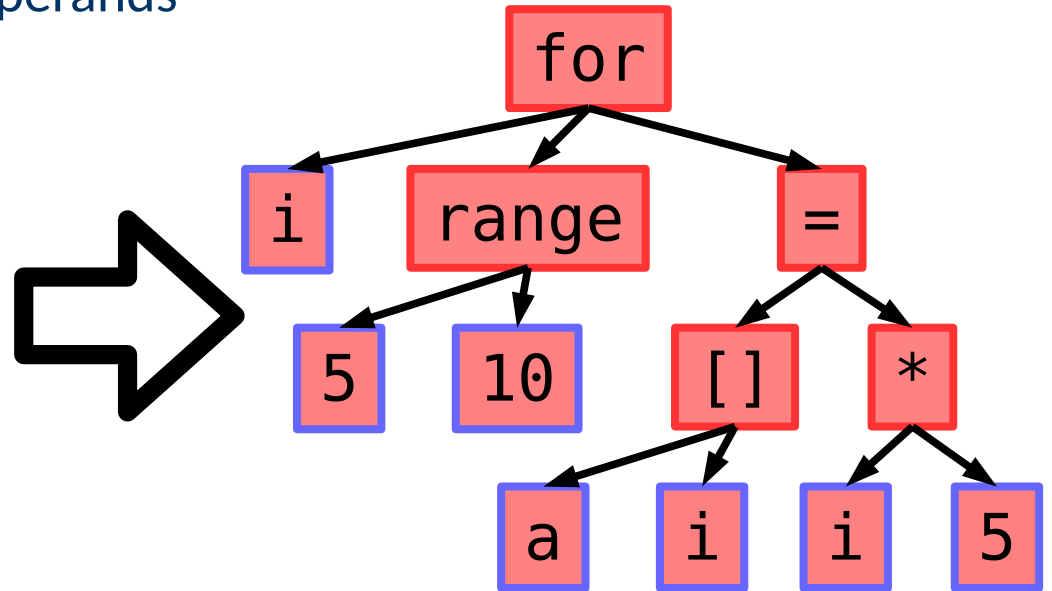
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 - **Leaves** are values, variables, operands

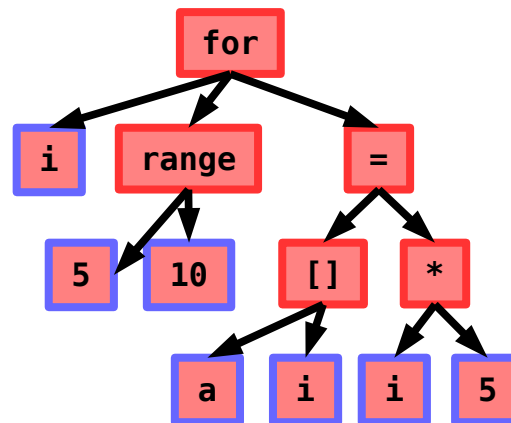
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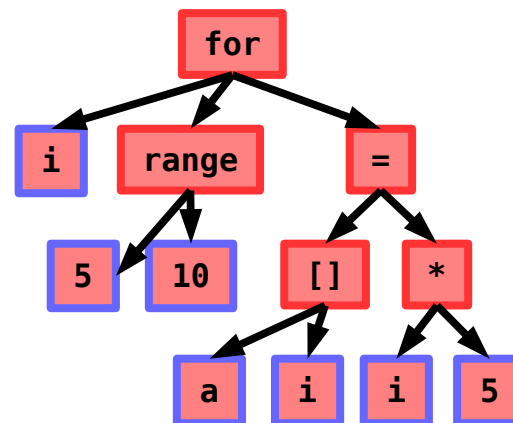
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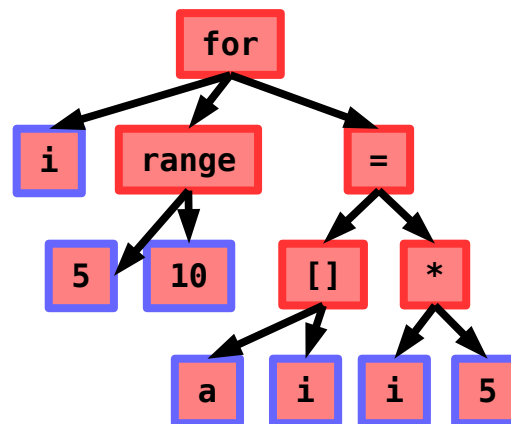
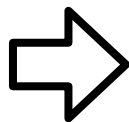
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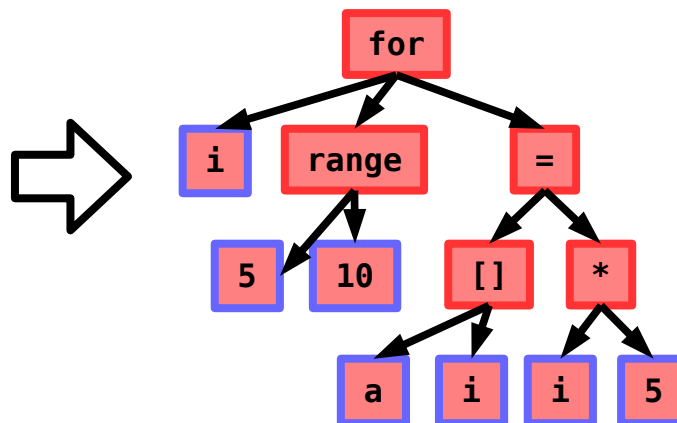
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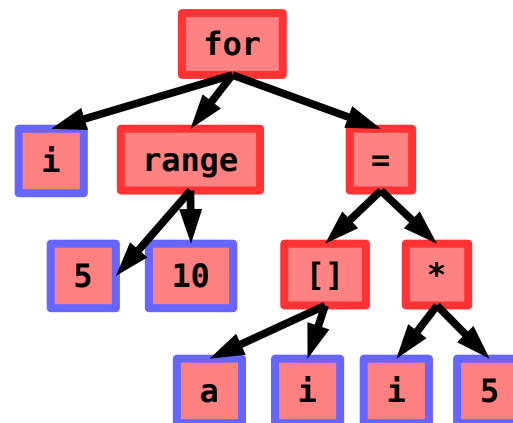
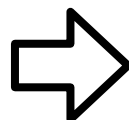
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 - Training prediction/completion models

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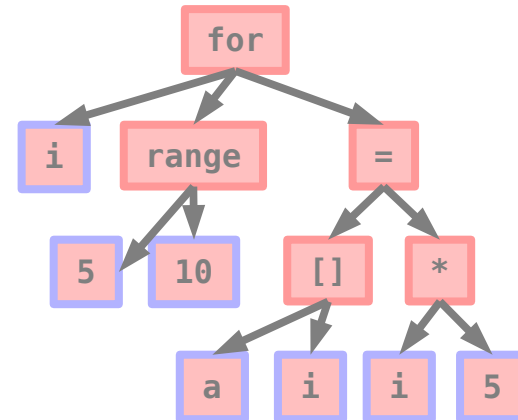
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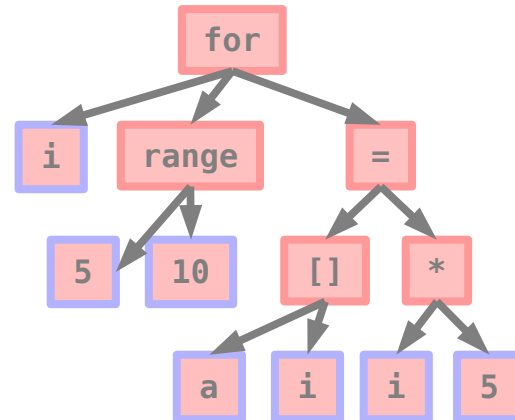
1) Abstract Syntax Trees

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But:

- The same program may still be spelled many ways
- Some information is *implicit* rather than *explicit*

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2) Control Flow Graphs

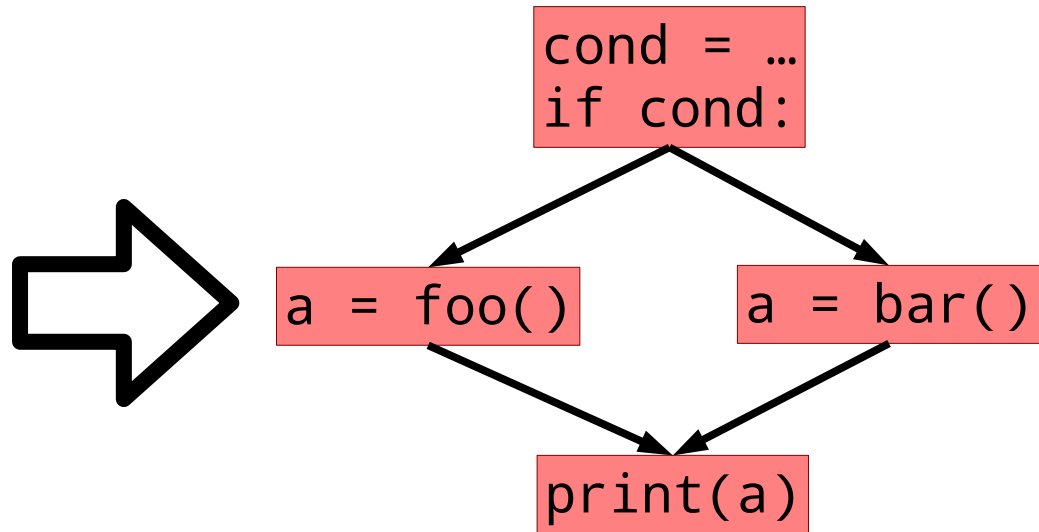
- Express the possible decisions and possible paths through a program

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cond = input()
if cond:
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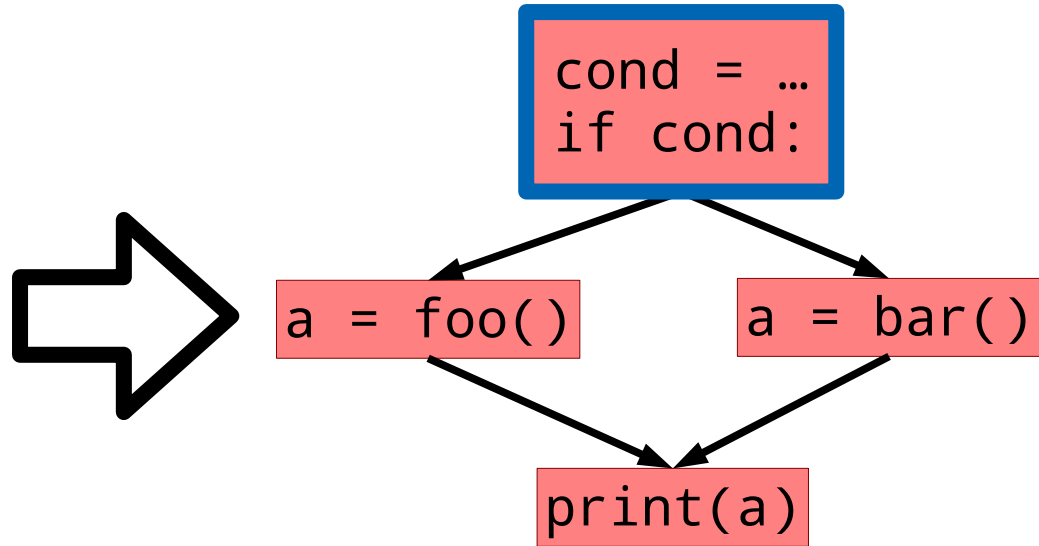
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 - **Basic Blocks** (Nodes) are straight line code

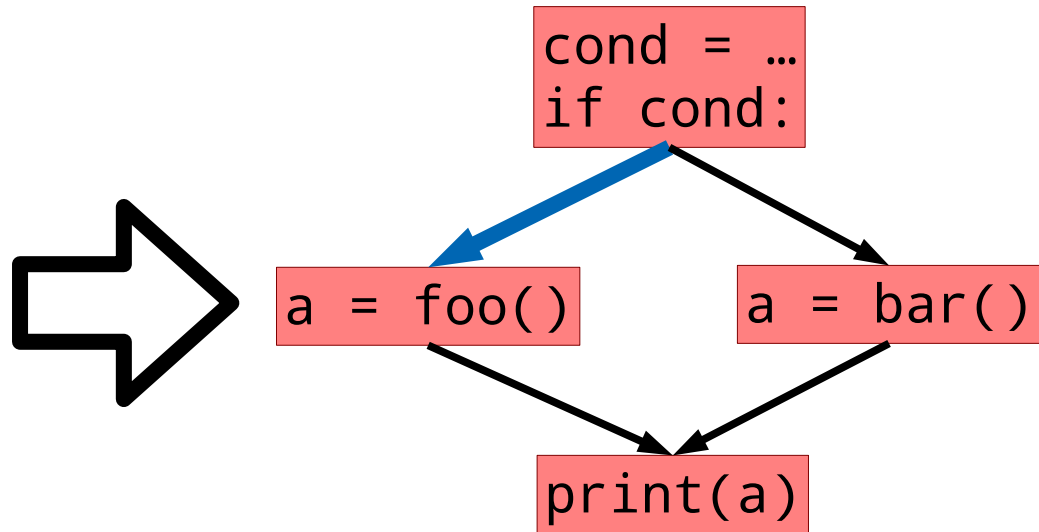
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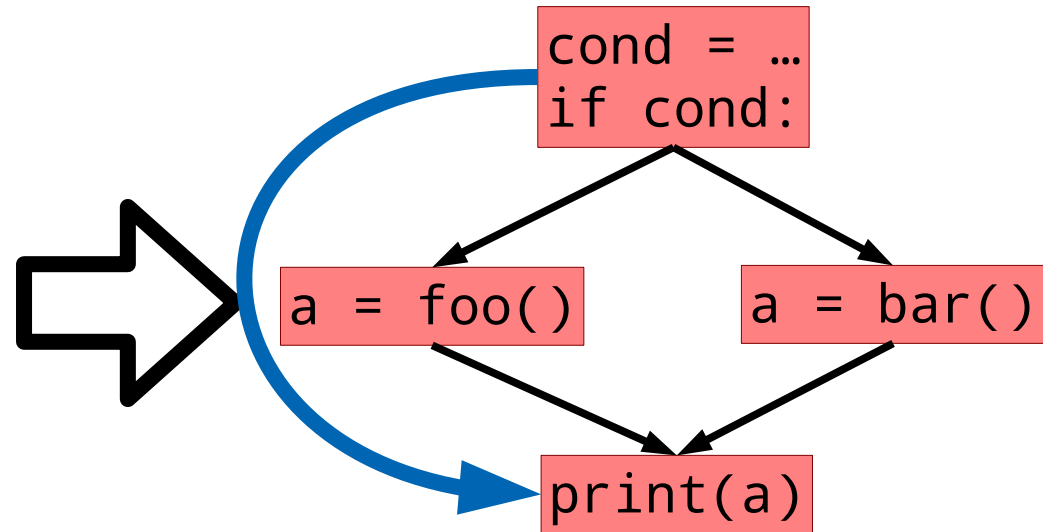
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2) Control Flow Graphs

- Express the possible decisions and possible paths through a program
 - *Basic Blocks* (Nodes) are straight line code
 - *Edges* show how decisions can lead to different basic blocks
 - *Paths* through the graph are potential paths through the program

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2) Control Flow Graphs (CFGs)

- Language specific features are often abstracted away

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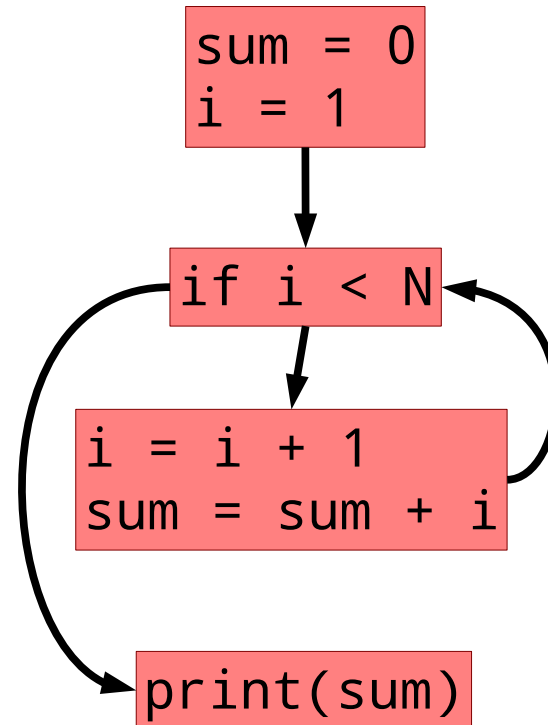
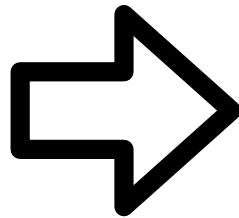
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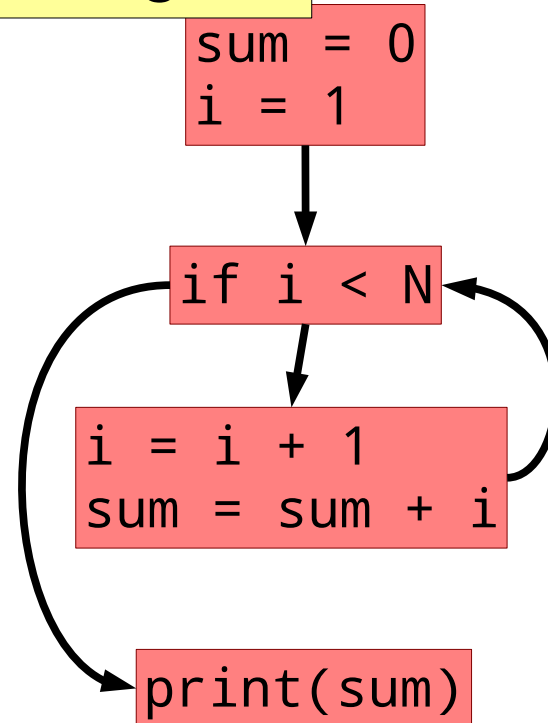
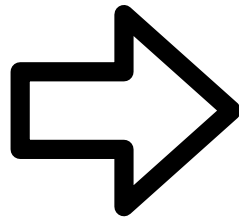


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The 'while' is gone

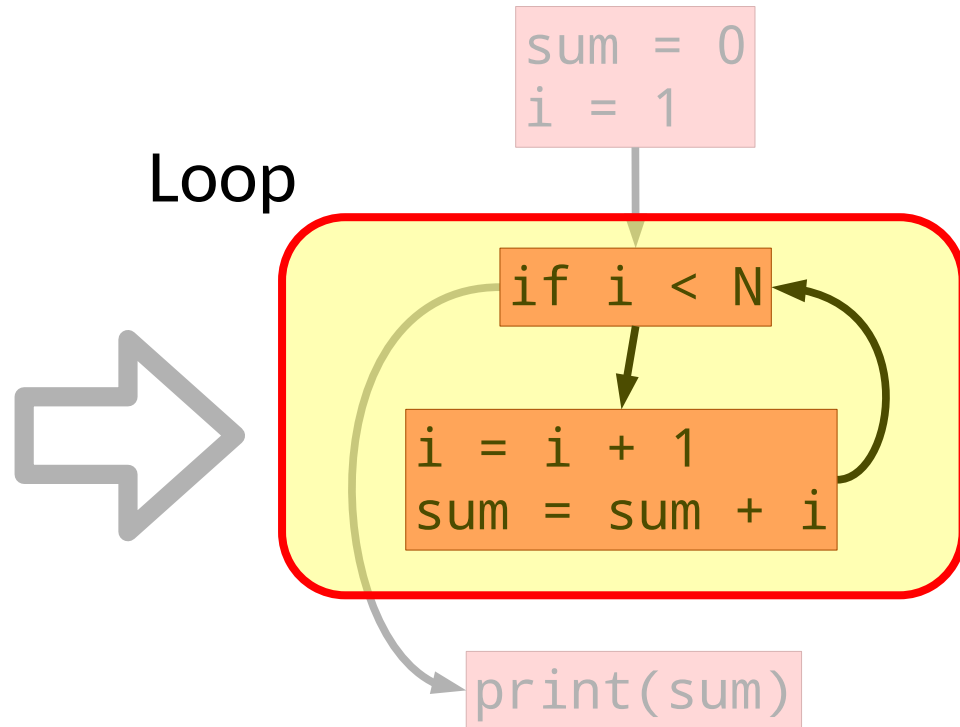
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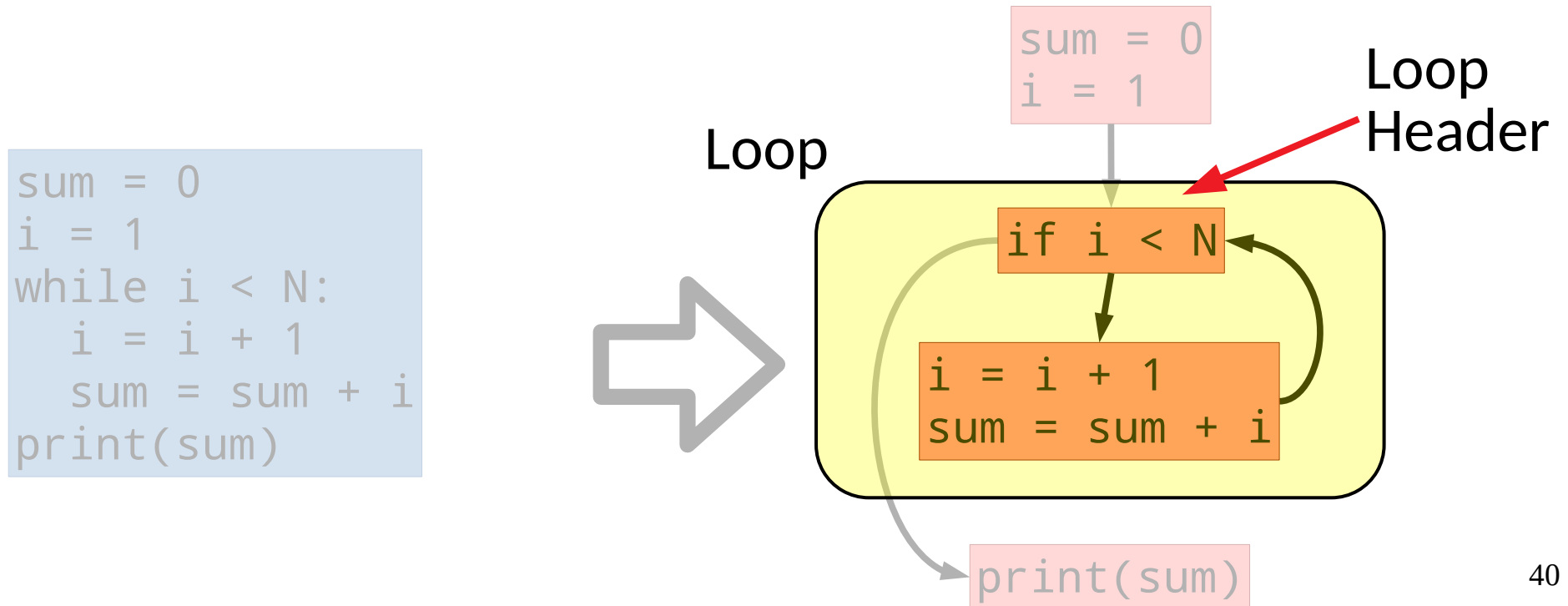
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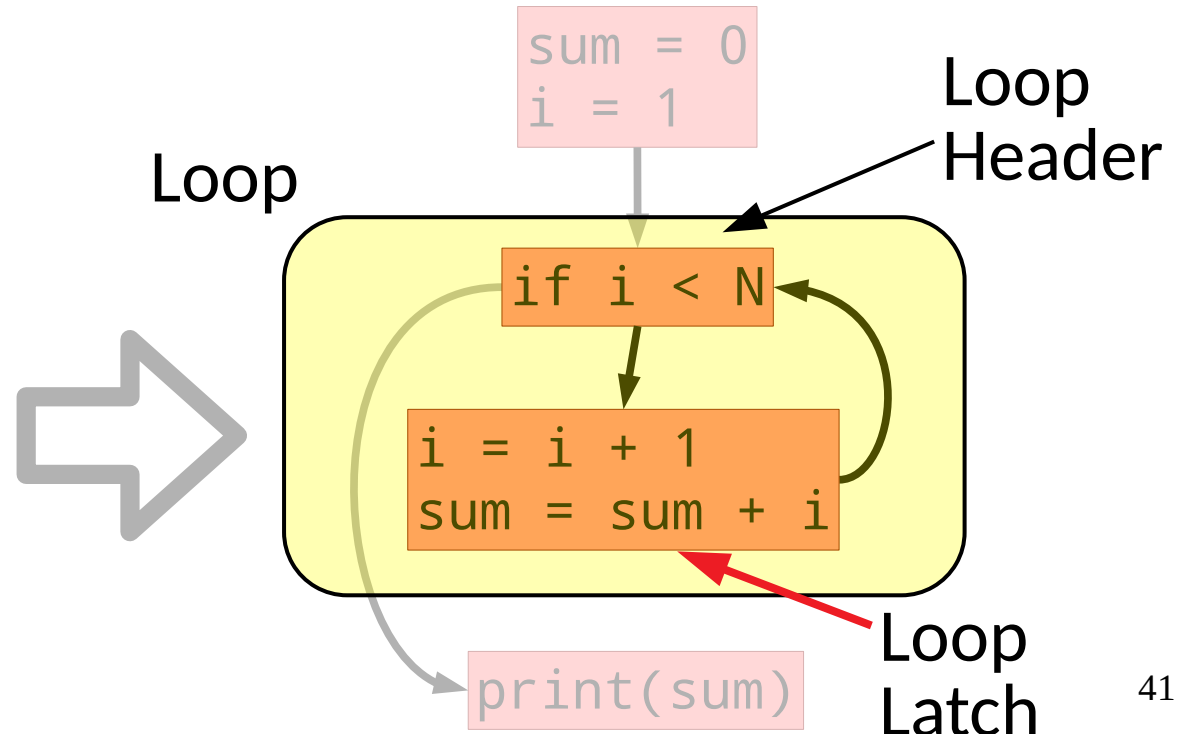
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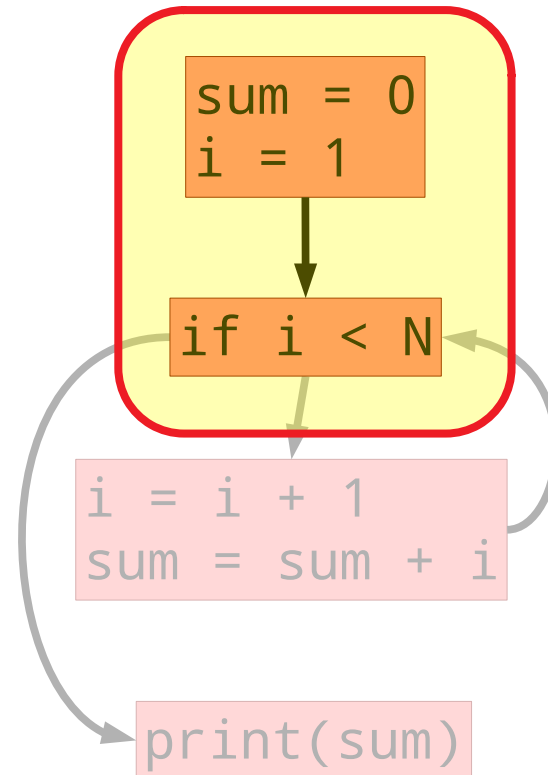
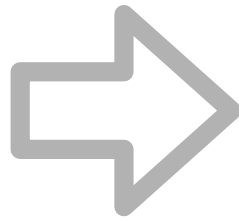


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Why is the 'if' in a separate block?

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print(sum)
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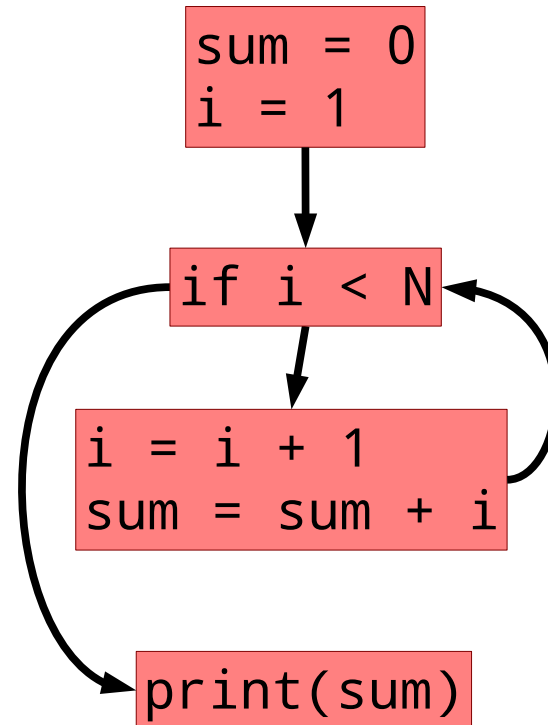
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What would the CFG of the equivalent 'for' look like?

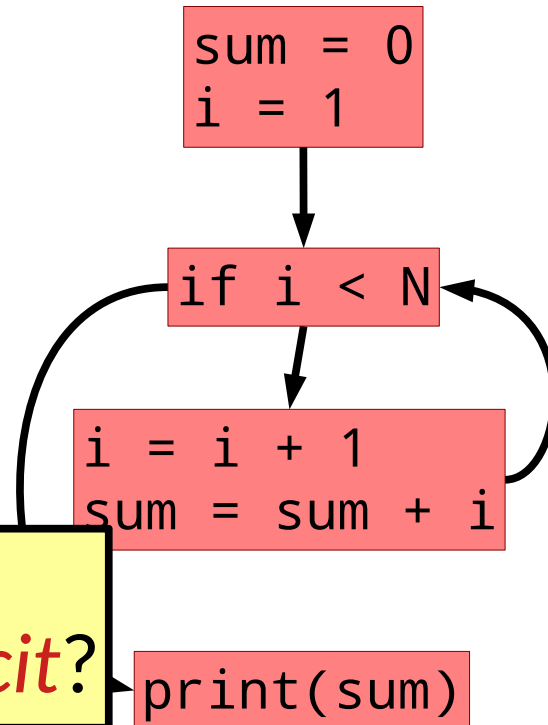
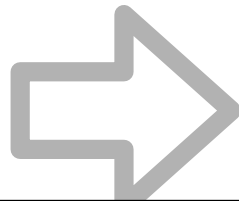
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What information is *explicit*?
What information is still *implicit*?

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- A *Program Dependence Graph* captures how instructions can influence each other
- Instruction X depends on Y if Y can influence X
 - Nodes are instructions
 - An edge $Y \rightarrow X$ shows that Y influences X

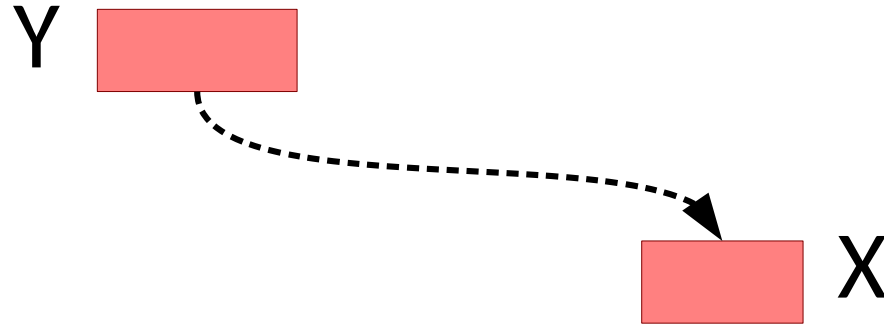
3) Program Dependence Graph (PDG)

- A *Program Dependence Graph* captures how instructions can influence each other
- Instruction X depends on Y if Y *can influence* X
- 2 main types of influence:
 - Data dependence
 - Control dependence

Data Dependence

X data depends on Y if

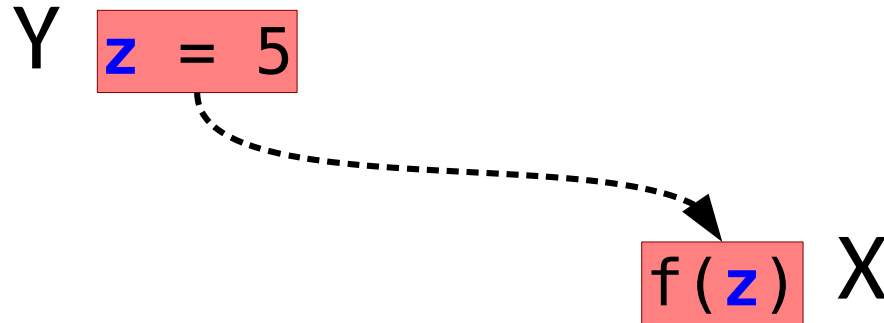
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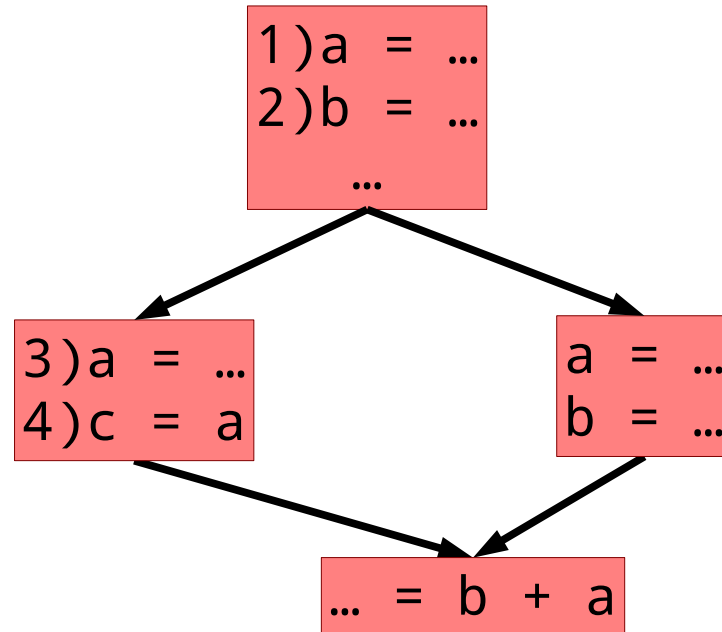
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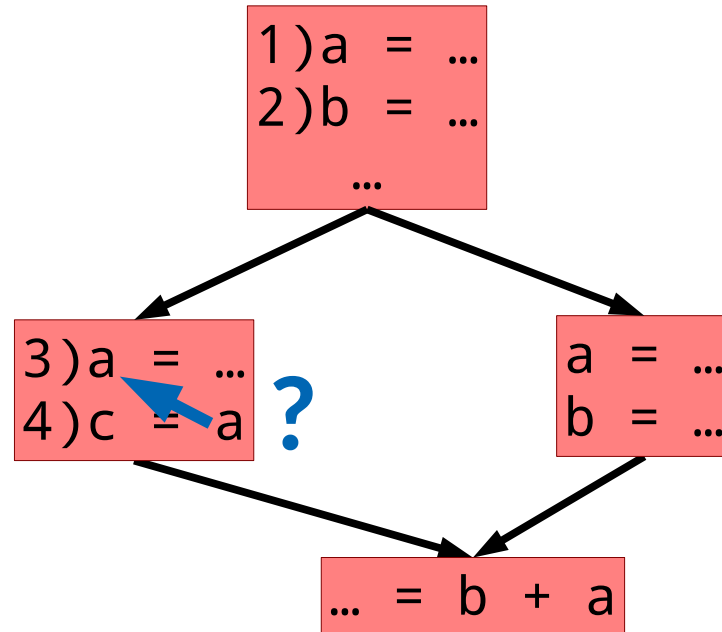
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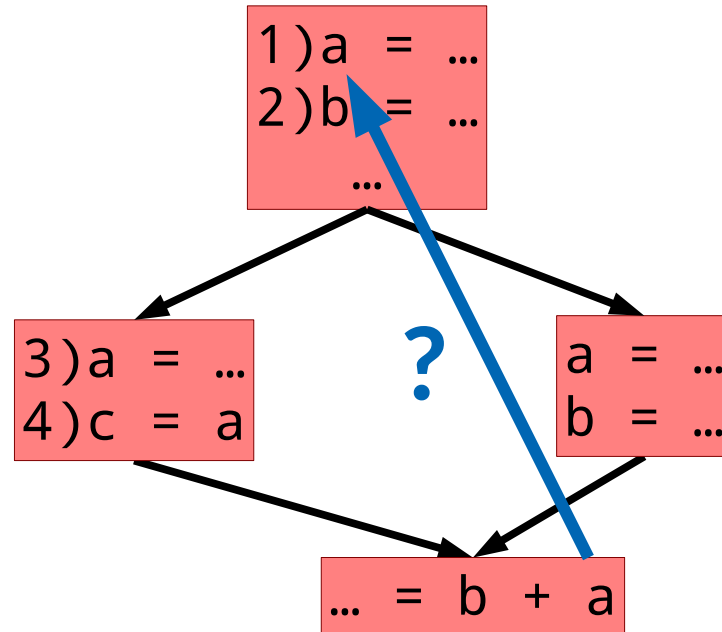
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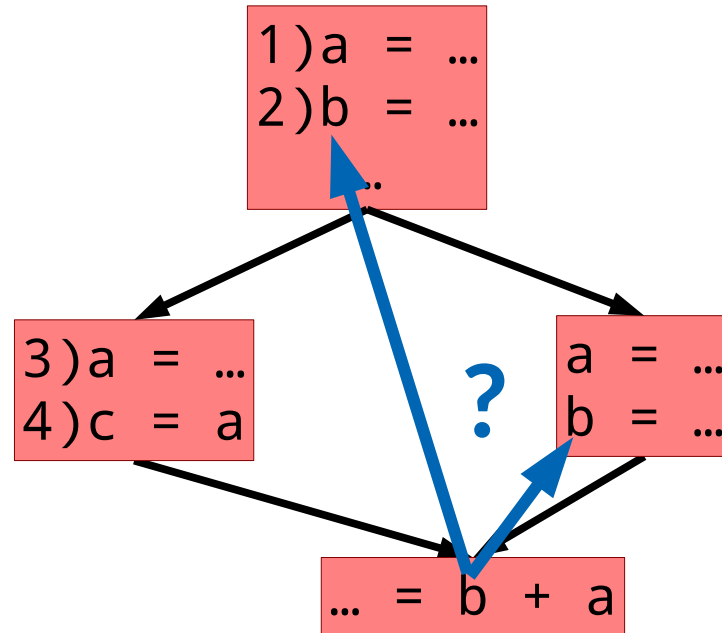
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Control Dependence

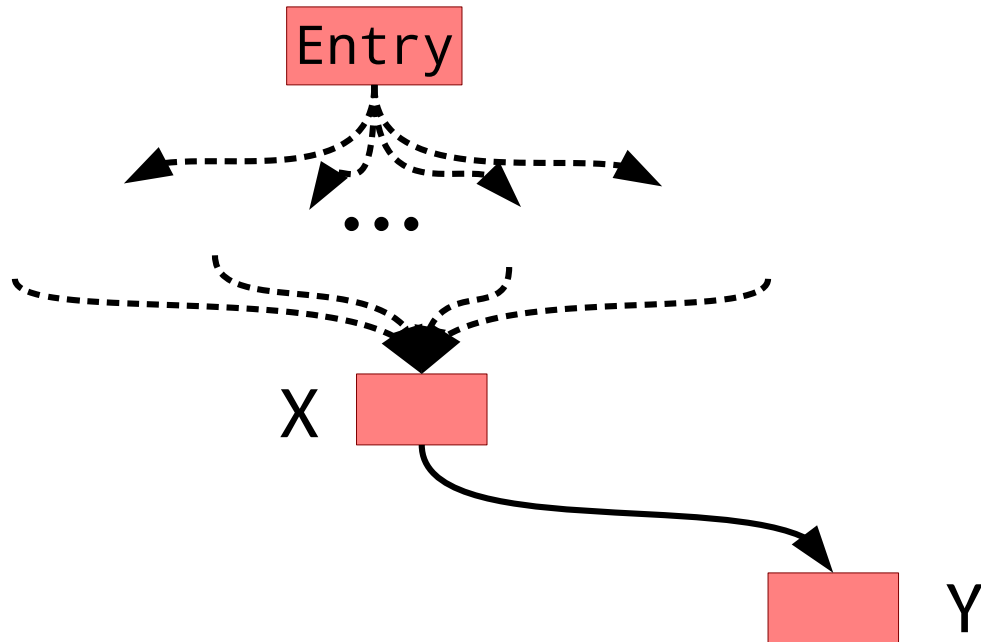
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- every path from the entry node to Y passes X
 - strict, normal, & immediate dominance

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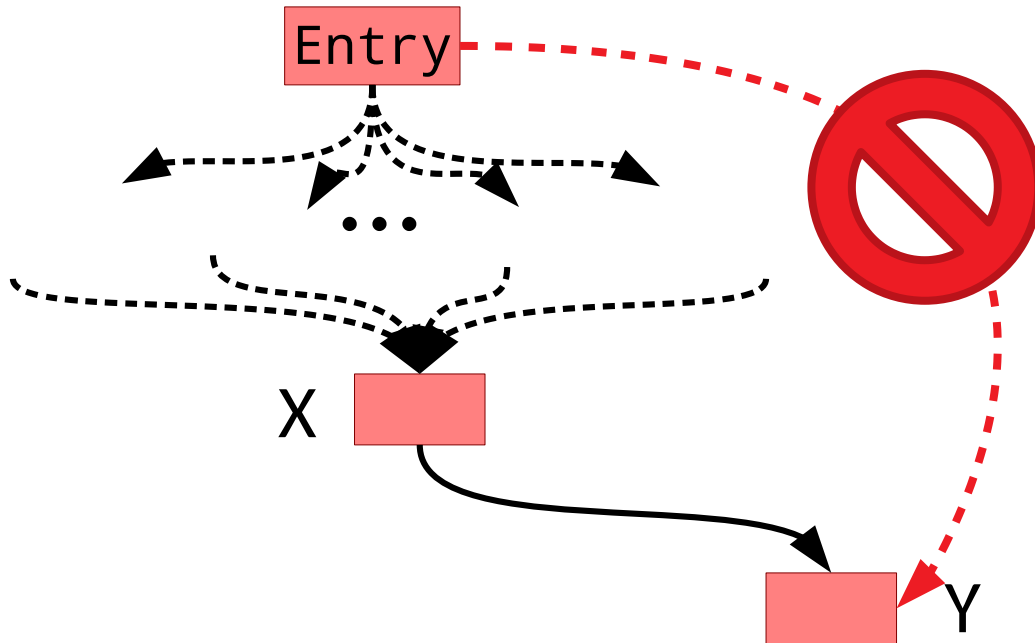
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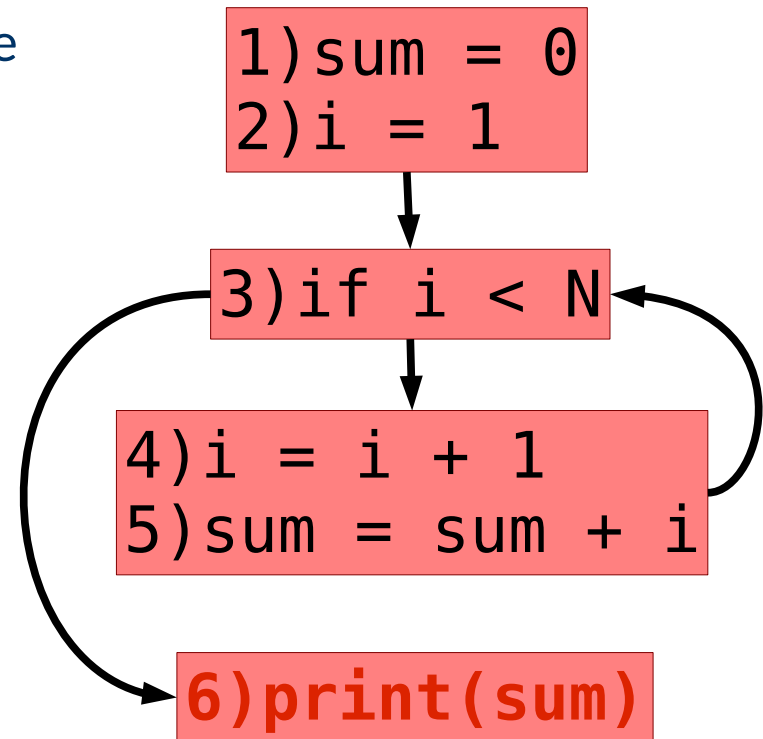
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2) i = 1
3) while i < N:
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6) print(sum)
```

DOM(6)= ?

IDOM(6)= ?



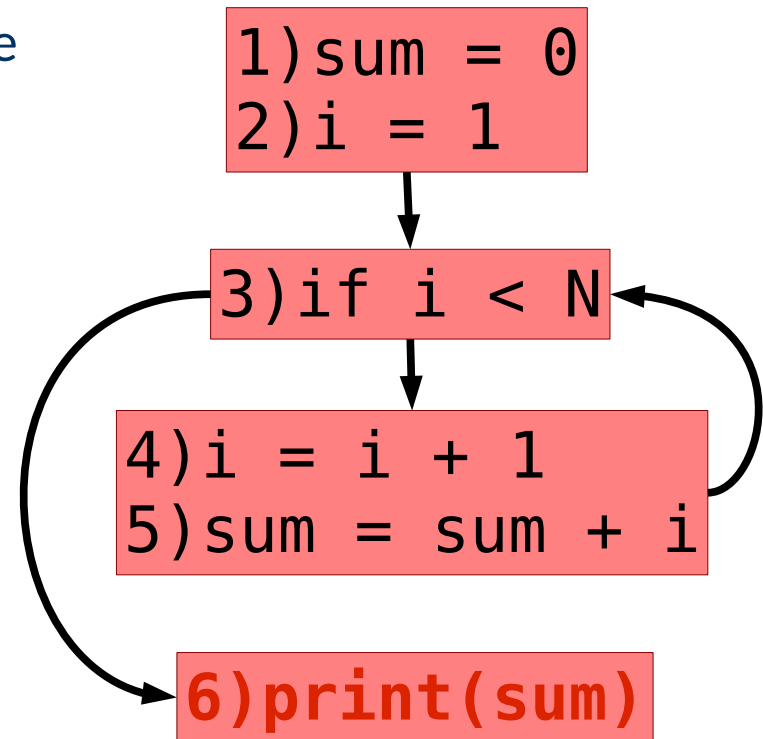
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$DOM(6) = \{1, 2, 3, 6\}$ $IDOM(6) = ?$



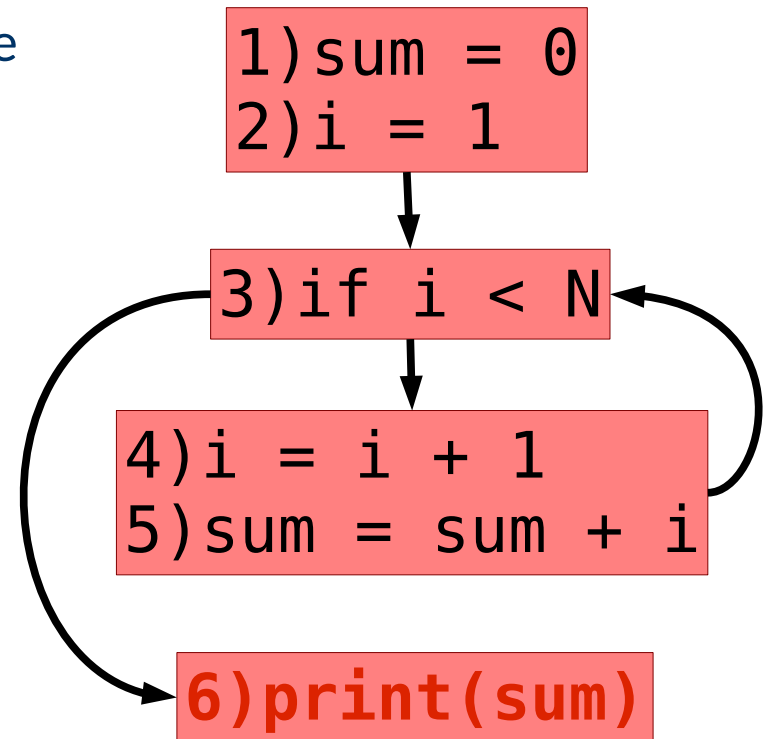
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What does this mean intuitively?

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1) sum = 0
2) i = 1
```

```
while i < N
```

```
4) i = i + 1
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```
6) print(sum)
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DOM(6)={1,2,3,6} IDOM(6)=3

Control Dependence

Preliminary: X **post** dominates Y if

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Control Dependence

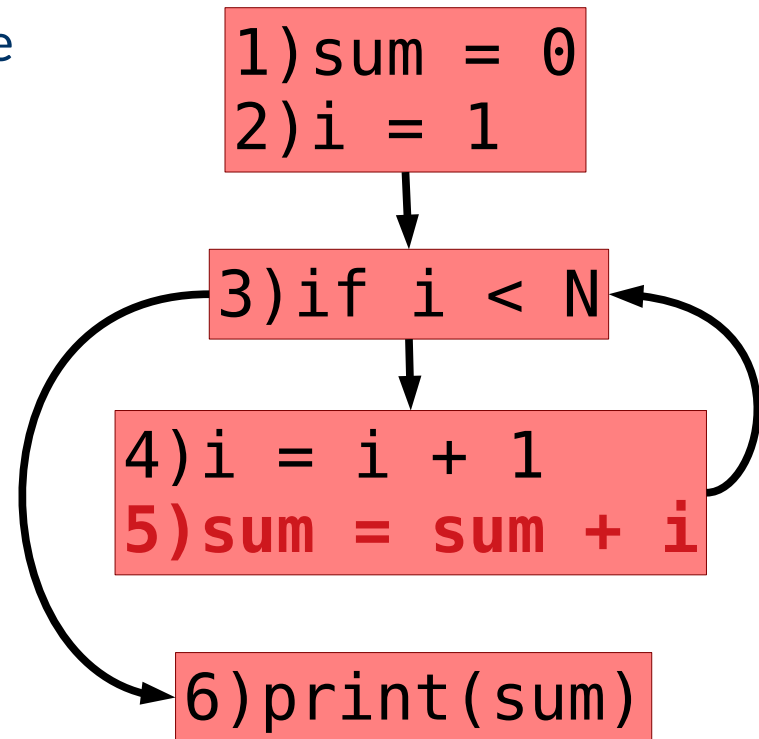
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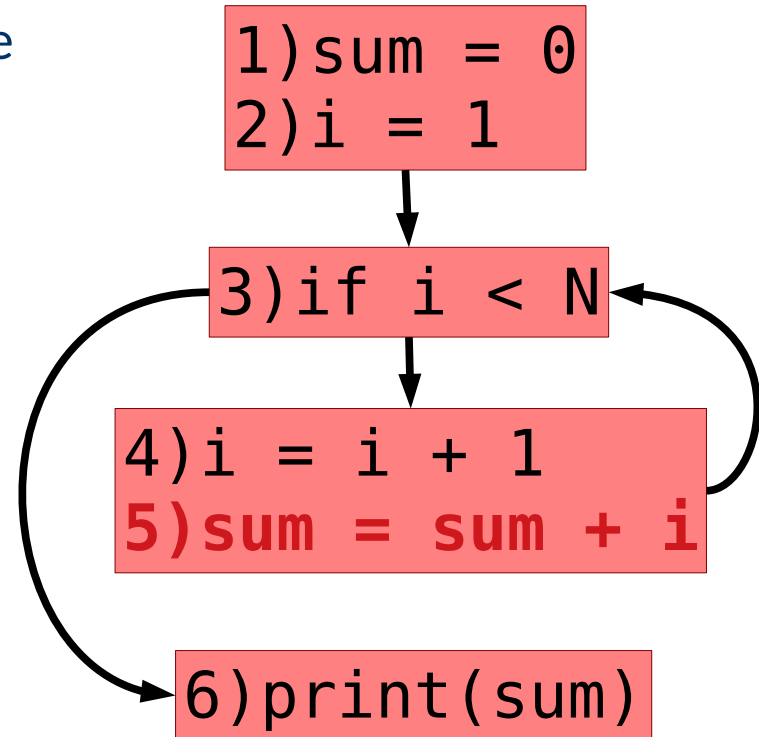
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$\text{PDOM}(5) = \{3, 5, 6\}$ $\text{IPDOM}(5) = ?$



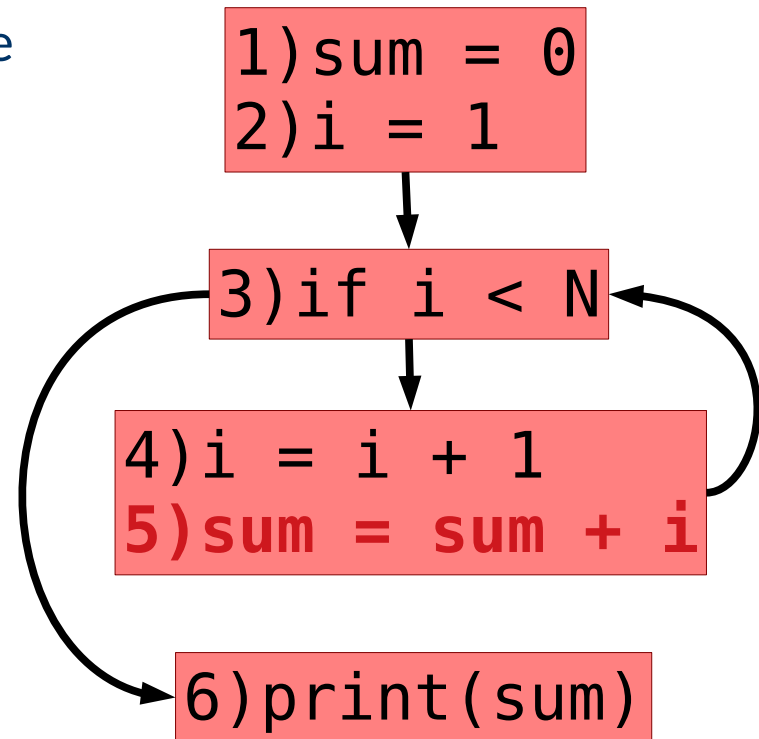
Control Dependence

Preliminary: X **post** dominates Y if

- every path from the **Y to exit** passes X
 - strict, normal, & immediate dominance

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1) sum = 0
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What does this mean intuitively?

```
1) sum = 0
2) i = 1
```

```
while i < N
```

```
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```

```
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Control Dependence (Finally)

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Control Dependence (Finally)

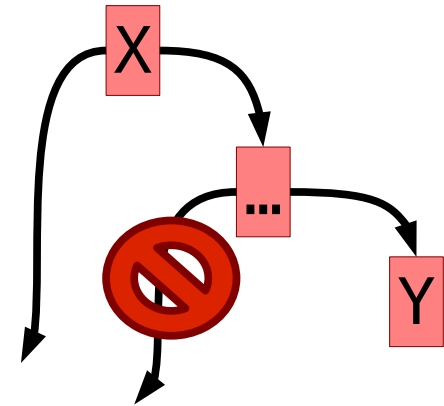
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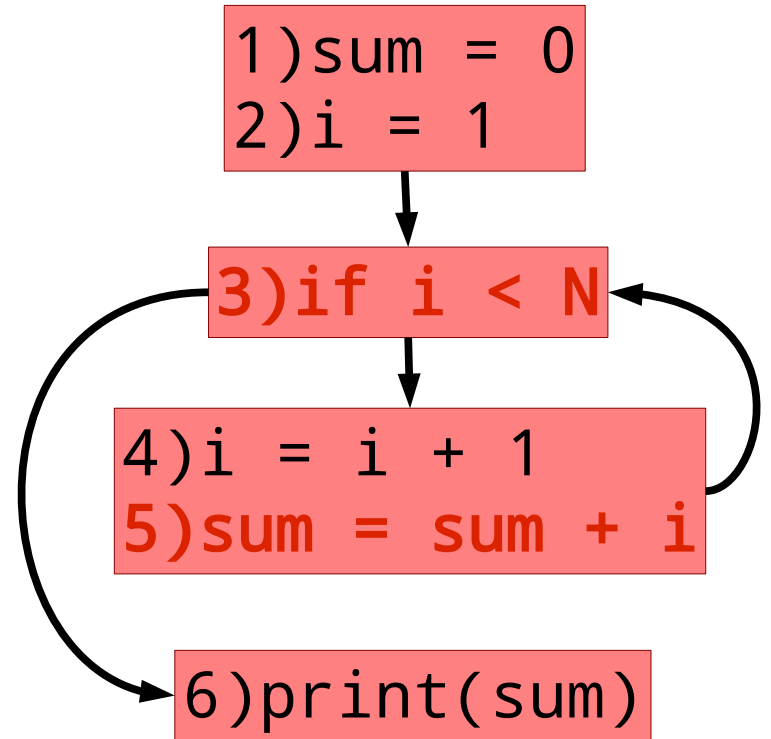
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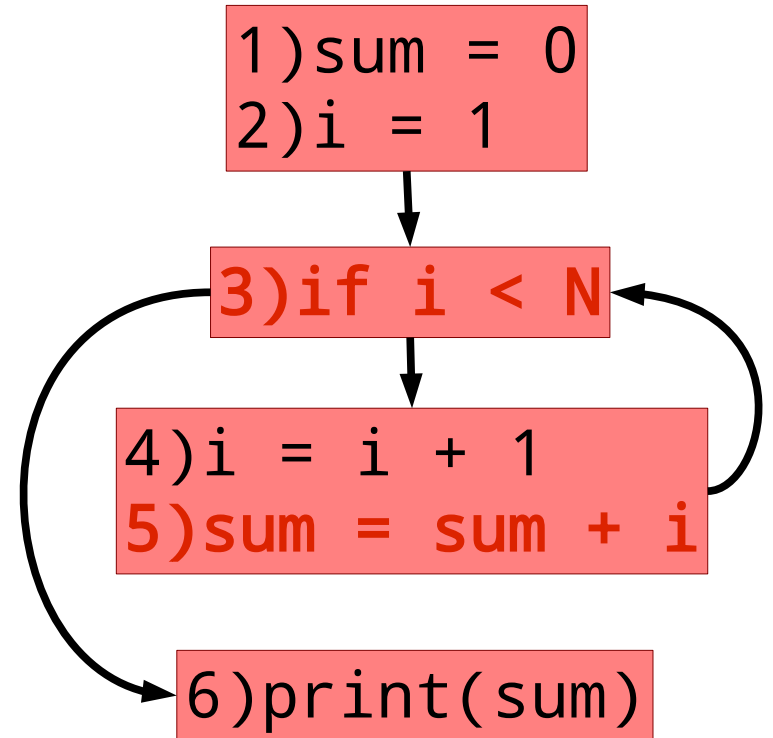


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What is CD(5)? CD(3)



Control Dependence

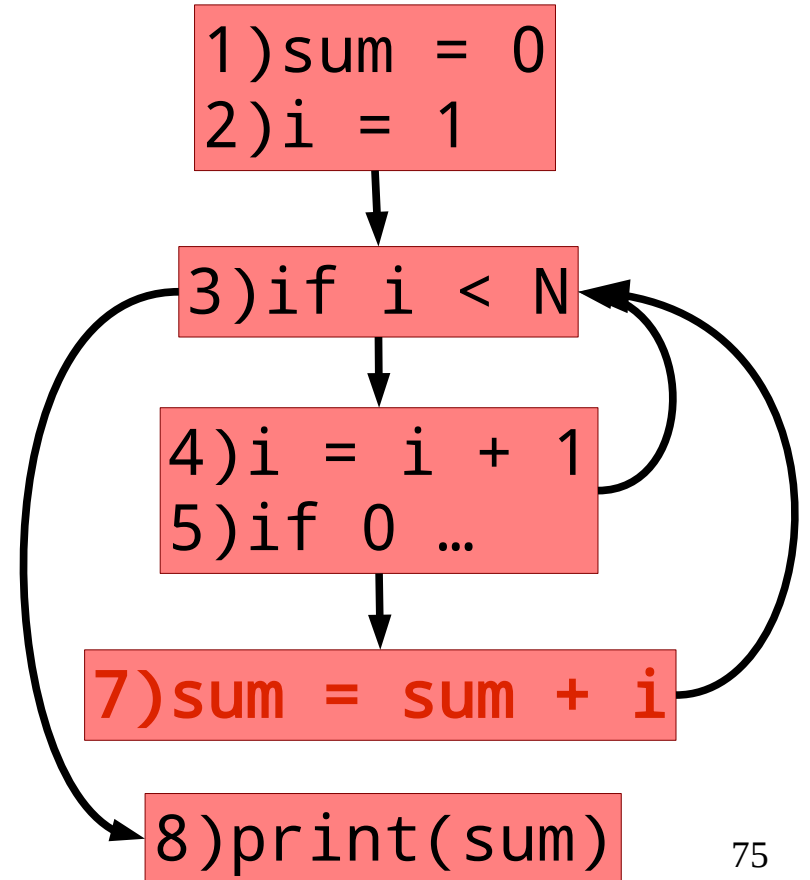
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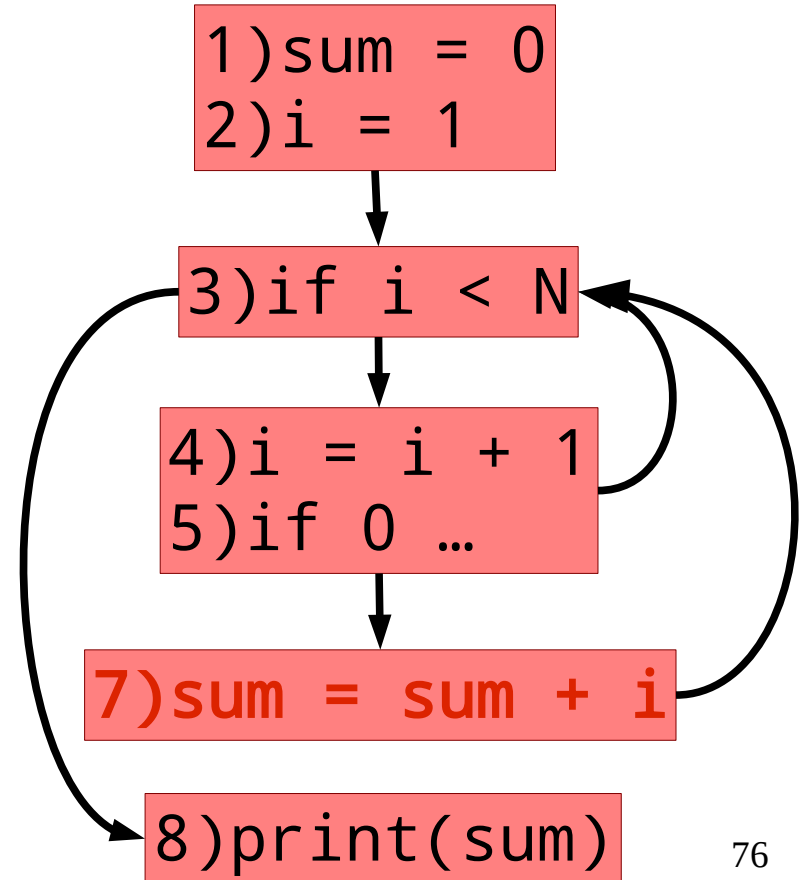


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What is CD(7)?



Control Dependence

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```
1)if X or Y:  
2)  print(X)  
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```

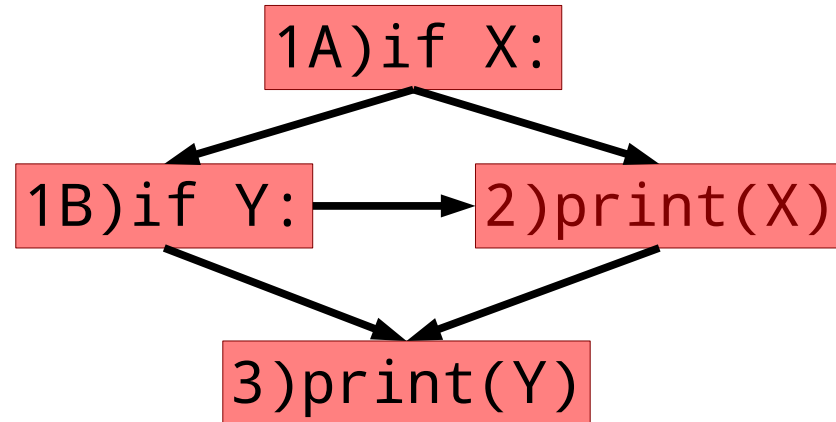
What is CD(2)?

Control Dependence

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```
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What is CD(2)?



3) Program Dependence Graph(PDG)

The PDG is the combination of

- The **control** dependence graph
- The **data** dependence graph

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Recall: Edges identify *potential influence*

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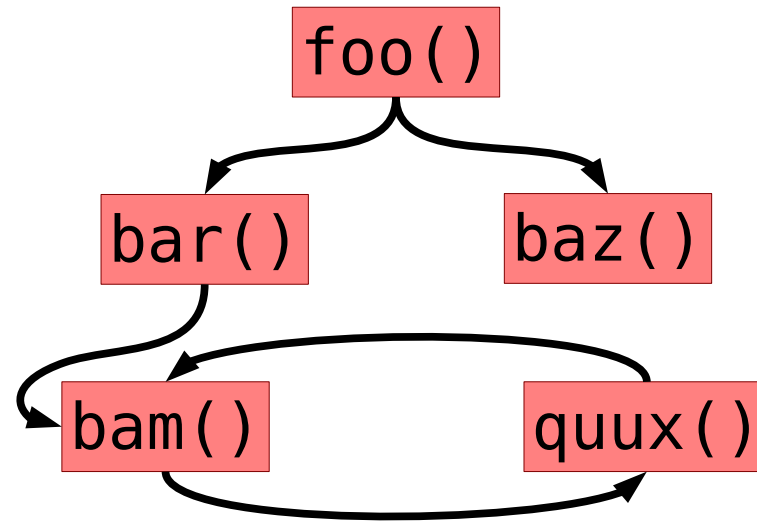
- The control dependence graph
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- **Debugging:** Can you see *challenges* that may arise when using the PDG in practice?
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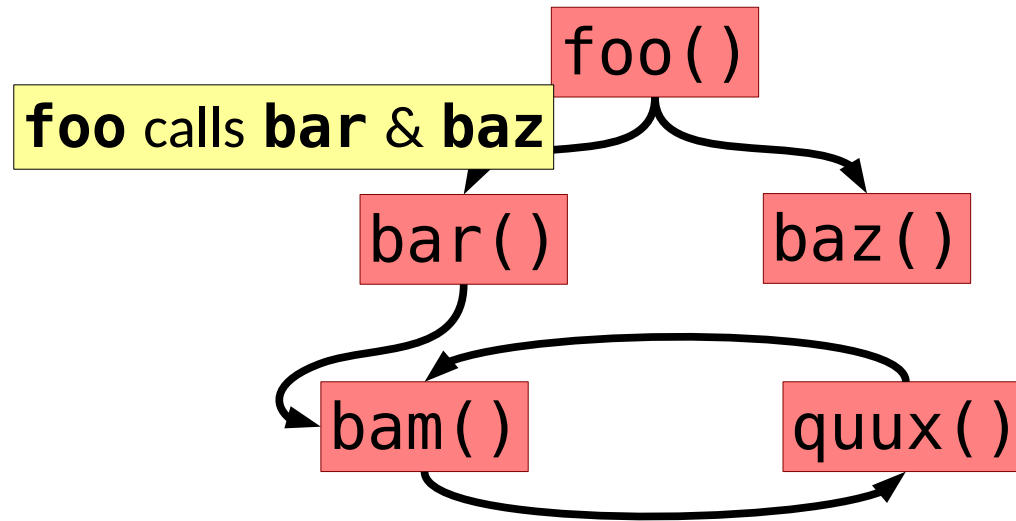
4) Call Graph (Multigraph)

- Captures the composition of a program
 - Nodes are functions
 - Edges show possible calls



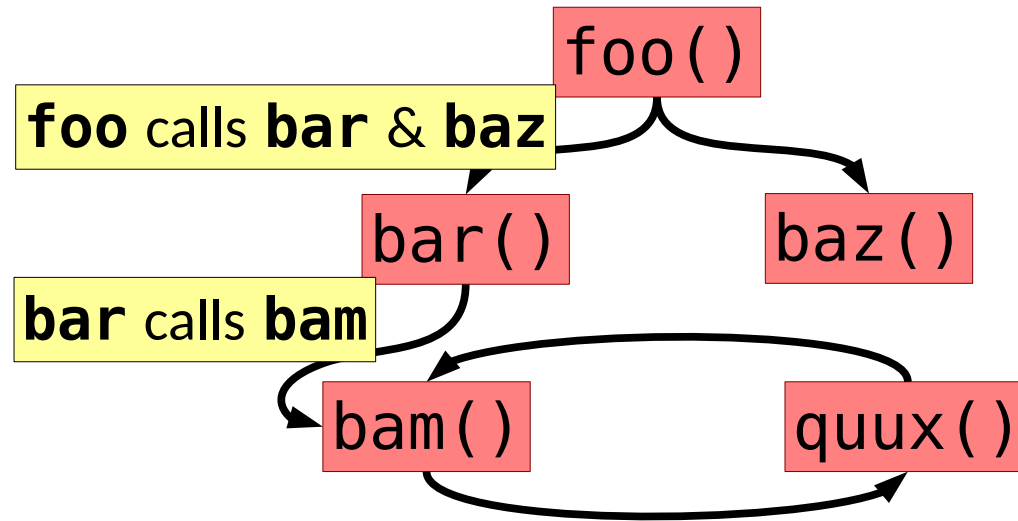
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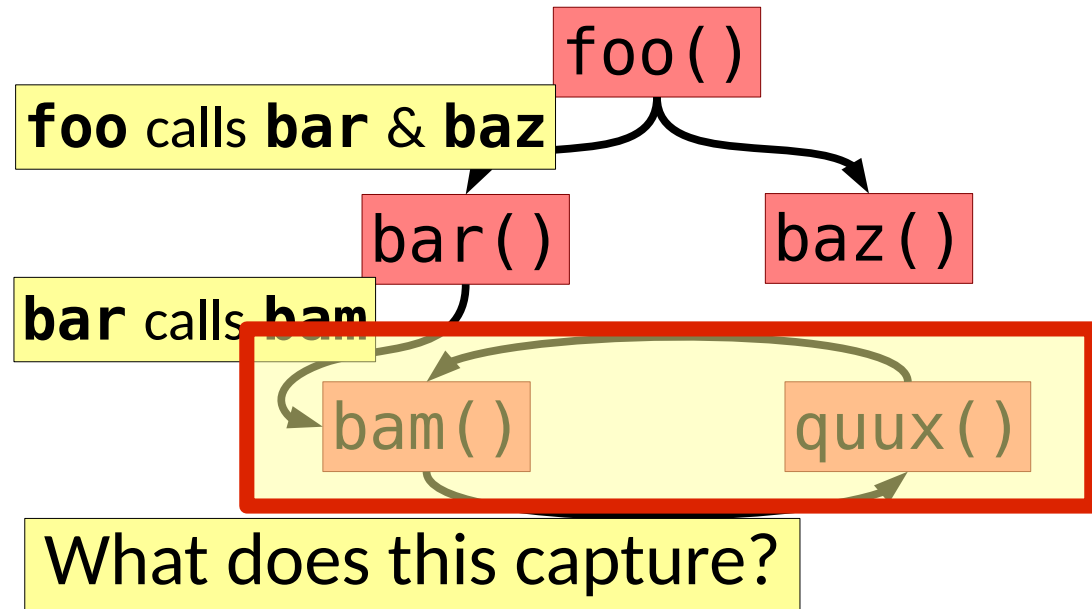
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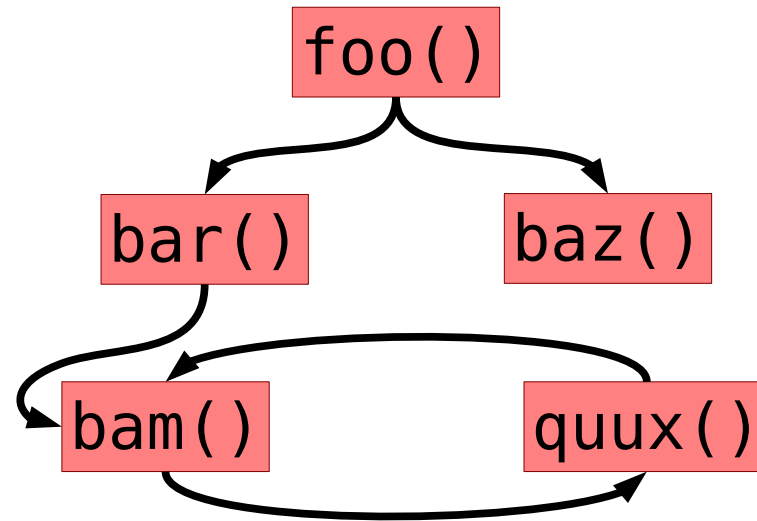
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How should we handle function pointers?



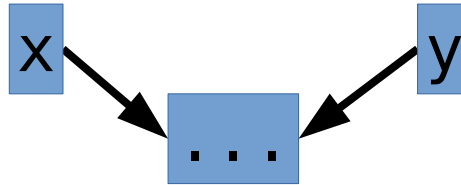
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Pointers / indirection create two difficult problems:

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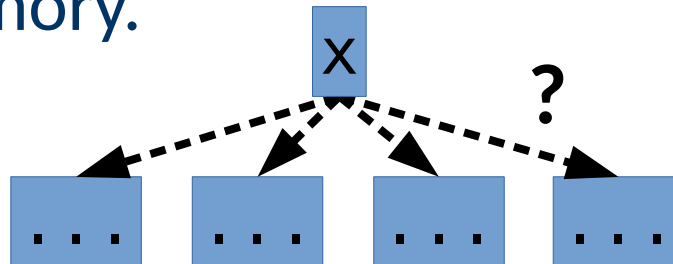
- **Aliasing**
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Pointers / indirection create two difficult problems:

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x.lock()  
...  
y.unlock()
```

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 - Multiple variables may denote the same memory location
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```
x.lock()  
...  
y.unlock()
```

```
x = password  
...  
broadcast(y)
```

5) Points-to Graphs

Points-to graphs capture this points-to relation

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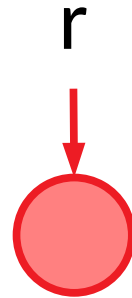
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2) p.f = r  
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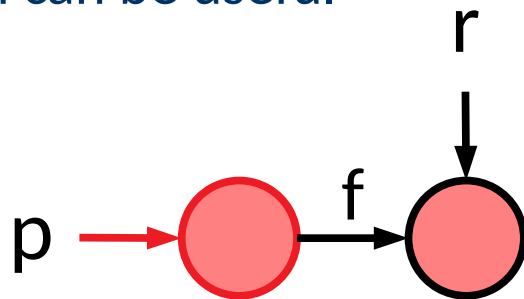


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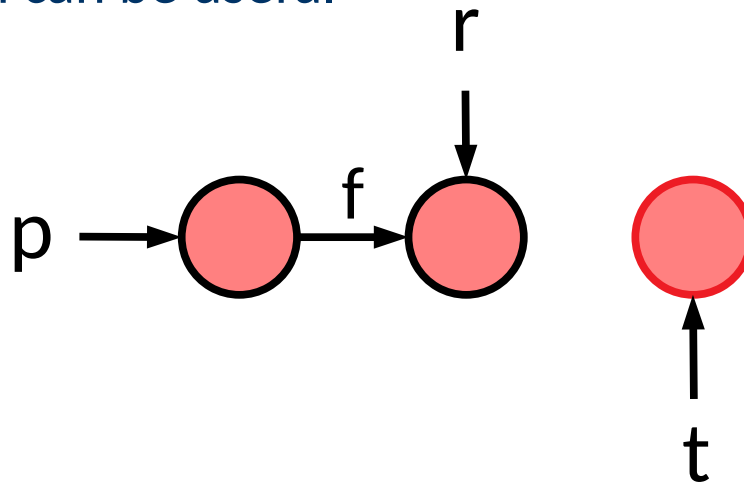


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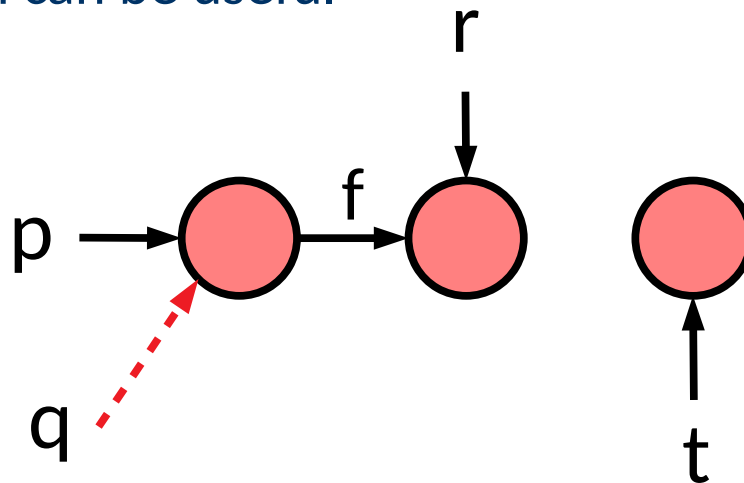


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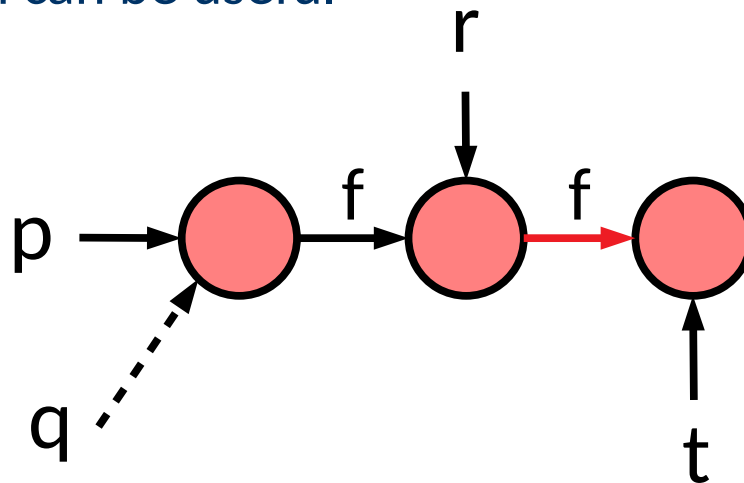


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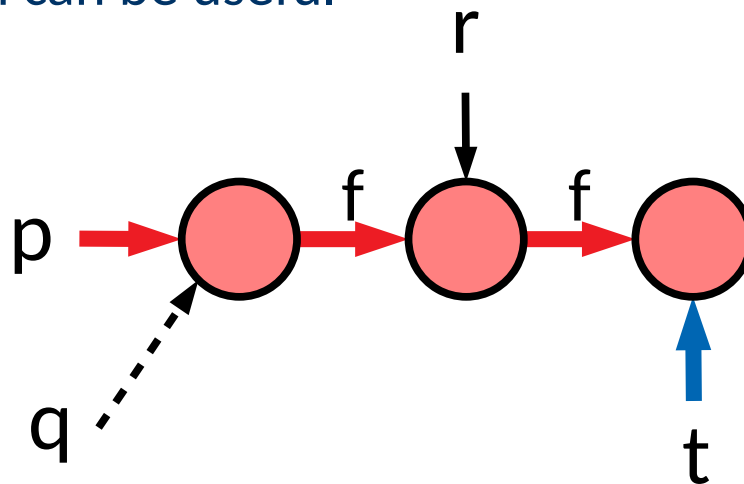


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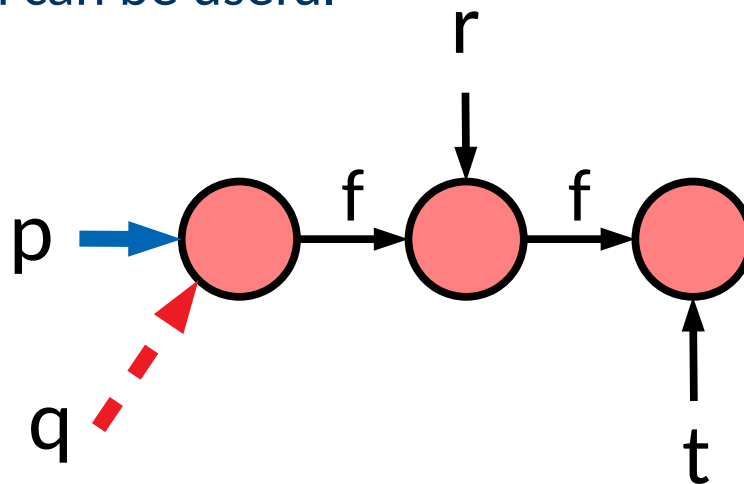
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- Machine learning is seen as a value driver for many tasks, but using it effectively to reason about software is still challenging

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- Trying simple models should always be considered first
e.g. simple feed forward networks can work better [Yedida 2021]

6) Emerging Representations for ML

- Machine learning is seen as a value driver for many tasks, but using it effectively to reason about software is still challenging
- **Trying simple models should always be considered first**
 - Bug fix & close time estimation [Yedida 2021]
 - Project planning & analytics [Krishna 2020]
 - Recognizing actionable compiler warnings [Yang 2020]

6) Emerging Representations for ML

- Machine learning is seen as a value driver for many tasks, but using it effectively to reason about software is still challenging
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- **Observe:**
Many engineering tasks require *discrete* & *symbolic* reasoning.
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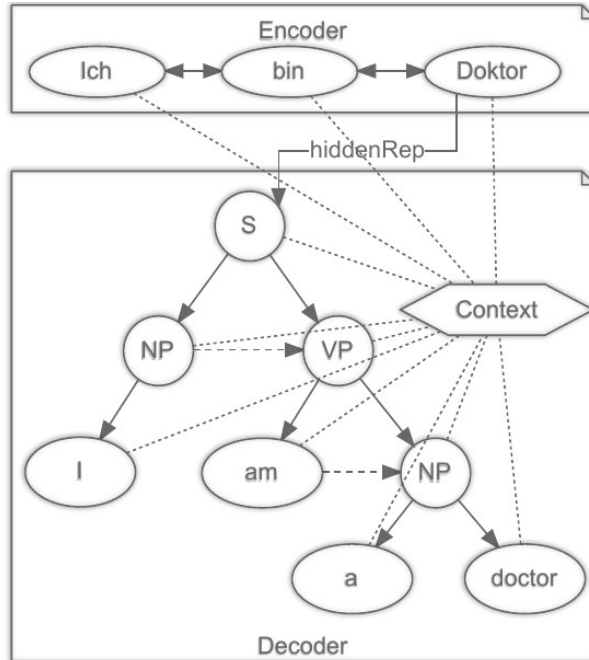
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 - Solutions that do not require *a priori* implementation are desirable
- But different models & pipelines arise to aid in reasoning about software

6) Emerging Representations for ML

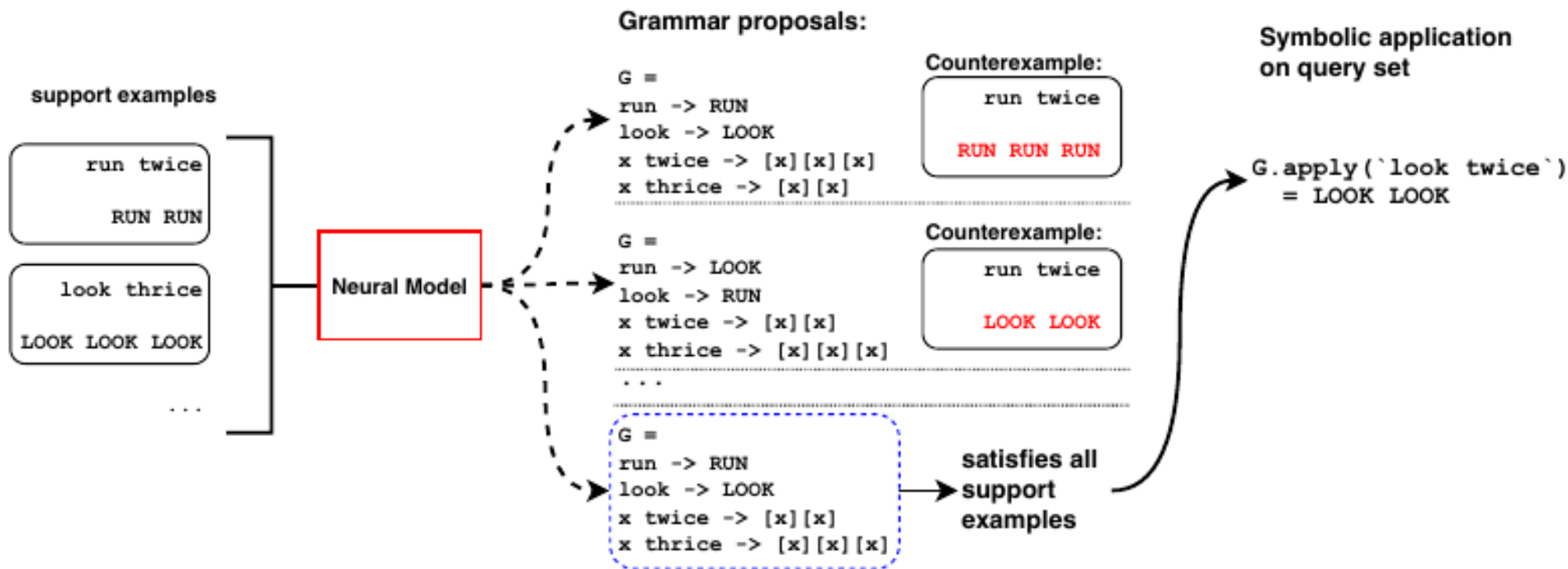
Seq2DRNN

Encoder-Decoders

[Alvarez-Melis 2017, Gu 2019]



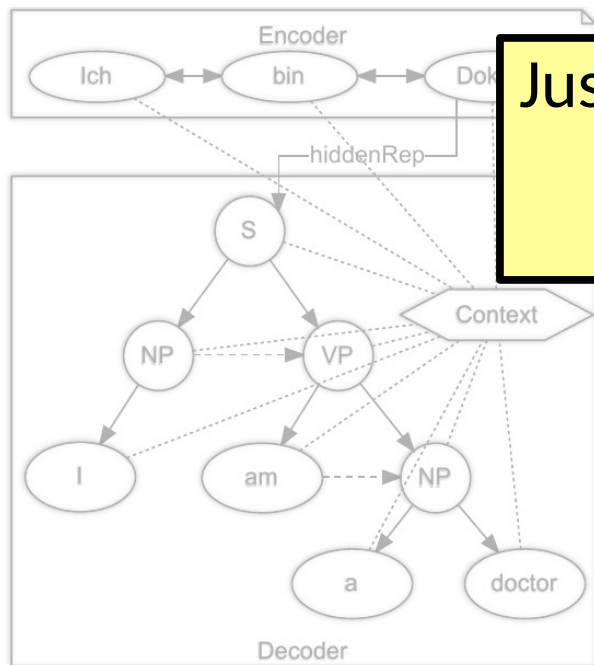
6) Emerging Representations for ML



neurosymbolic models for synthesis
[Nye 2020]

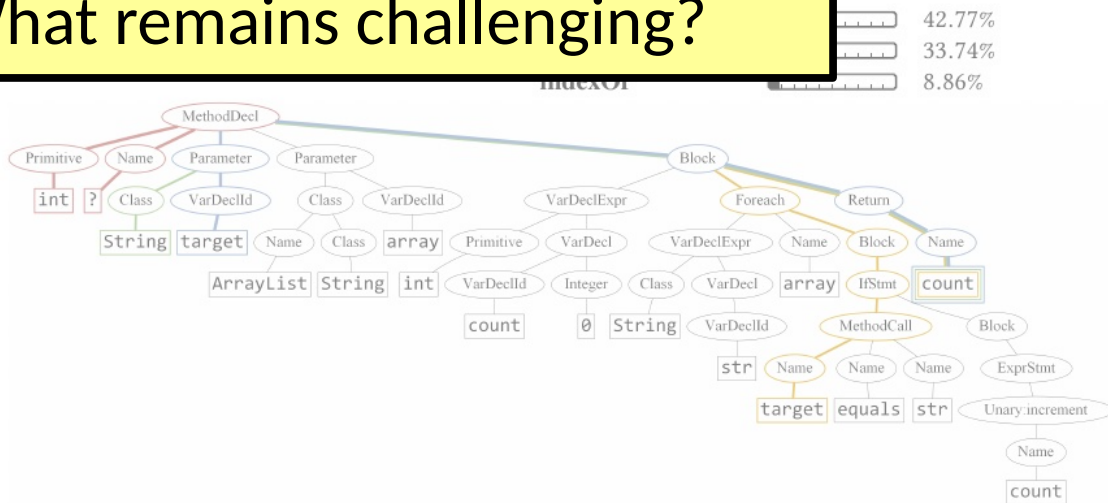
6) Emerging Representations for ML

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code2vec
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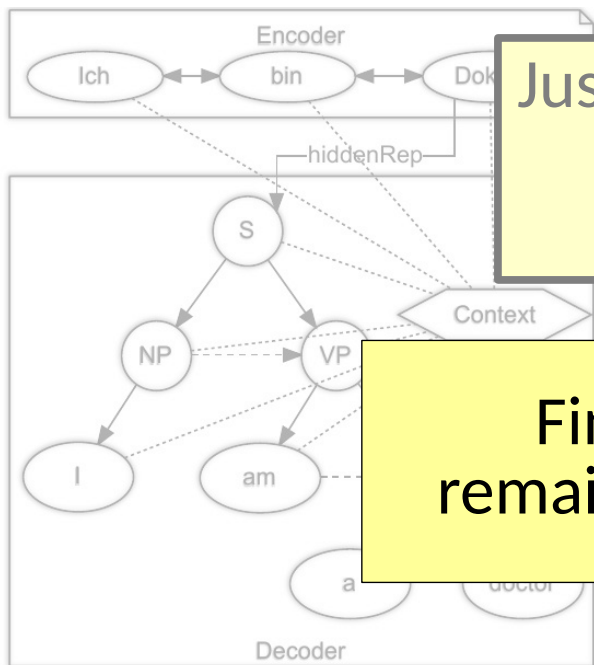
Just as with other representations:
What do these make easy?
What remains challenging?



6) Emerging Representations for ML

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code2vec
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Just as with other representations:
What do these make easy?
What remains challenging?

Finding ways to bridge ML and SE
remains an interesting & open challenge

42.77%
33.74%
8.86%



Representing Program Executions

Execution Representations

- **Program** representations are *static*
 - All possible program behaviors at once
 - Usually projected onto the CFG

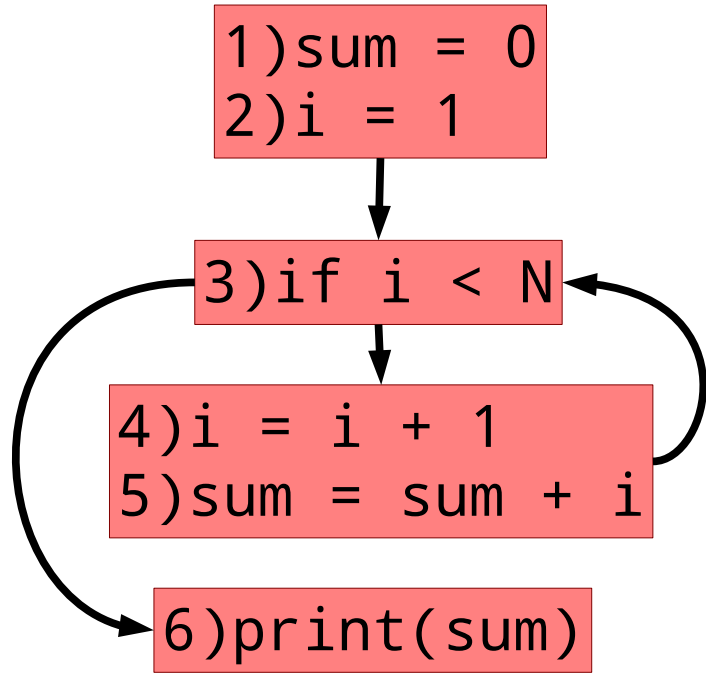
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 - Multiple instances of an instruction occur multiple times

Control Flow Trace



$1_1 2_1 3_1 4_1 5_1 3_2 4_2 5_2 3_3 6_1$ } All Equivalent

1) sum = 0
2) i = 1

3) if i < N

4) i = i + 1
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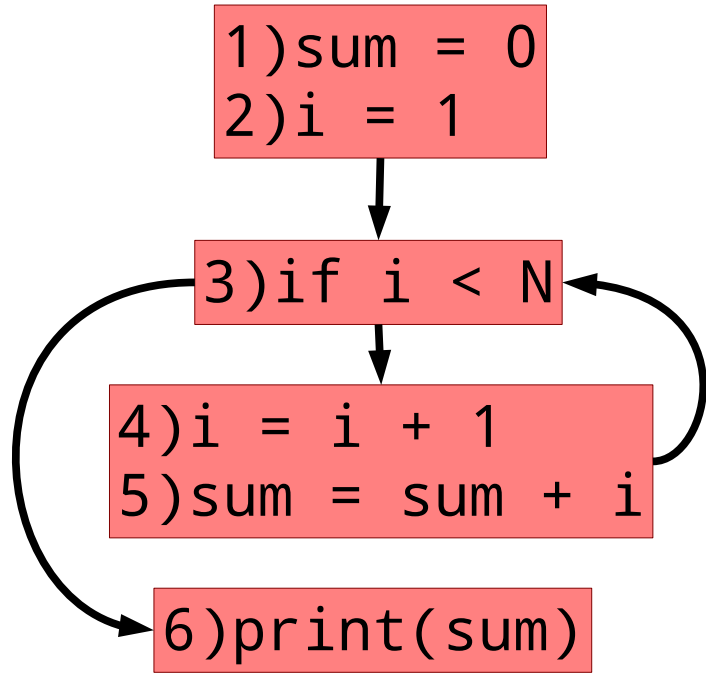
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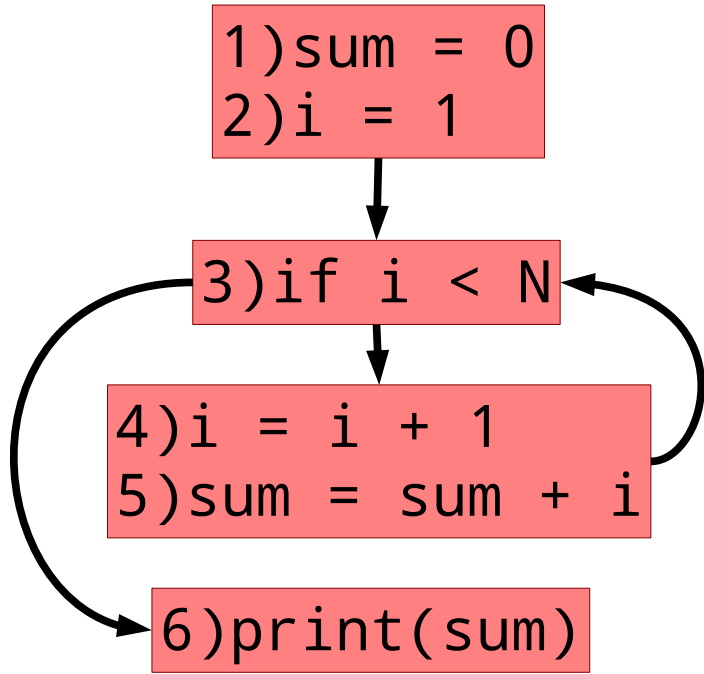
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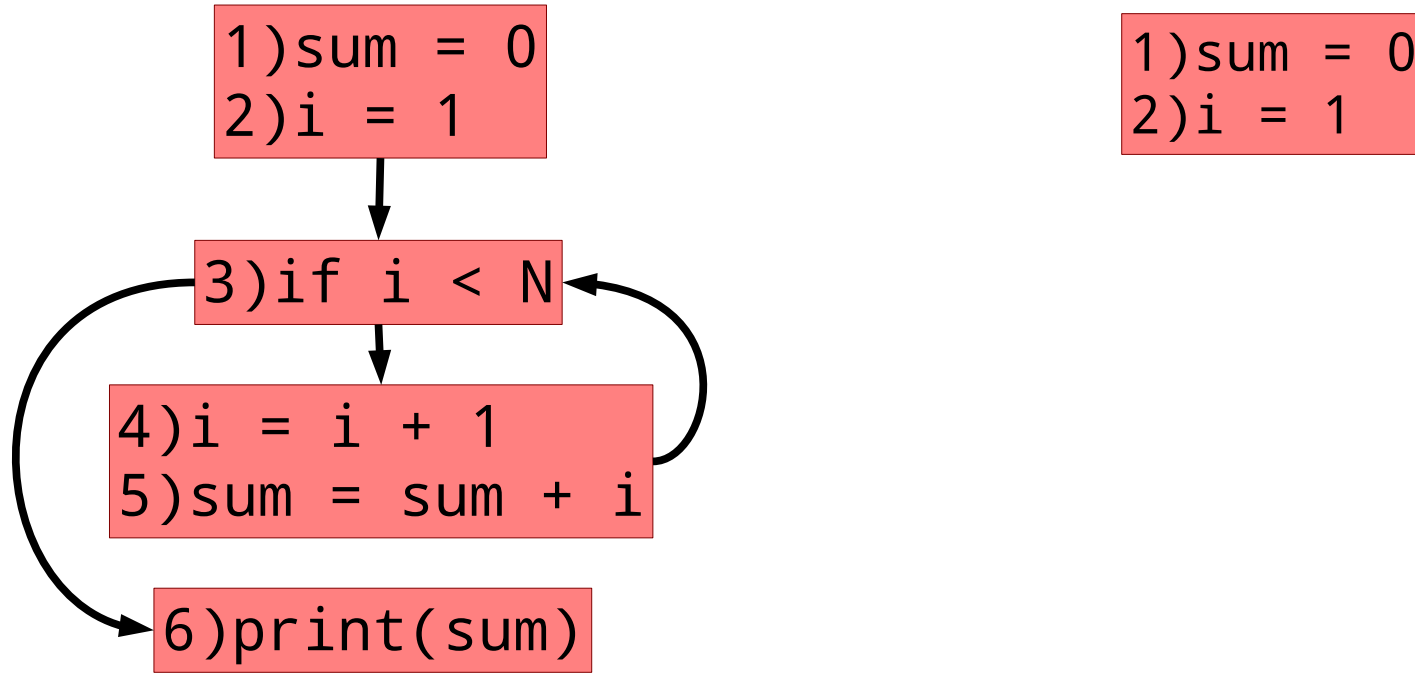
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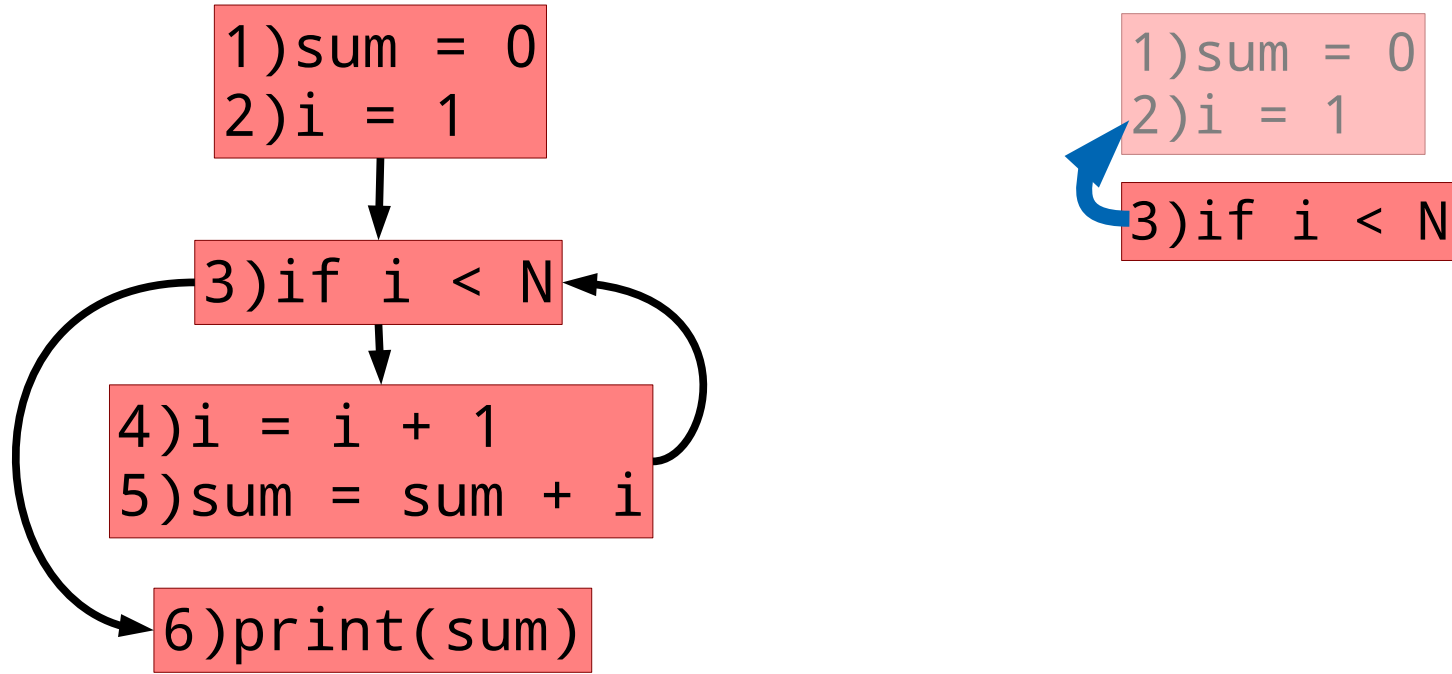
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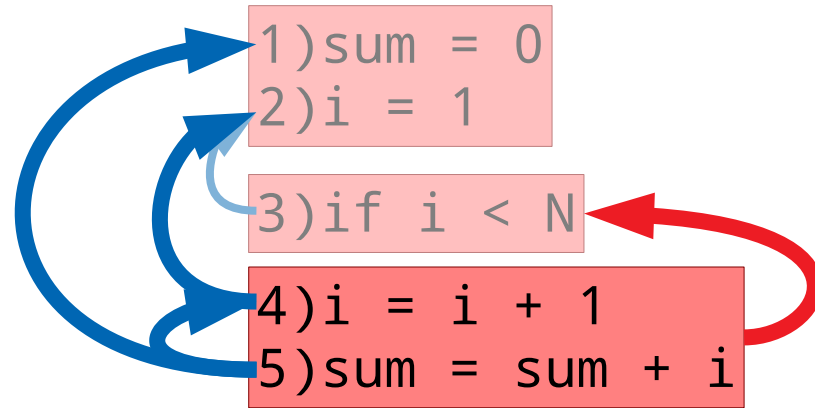
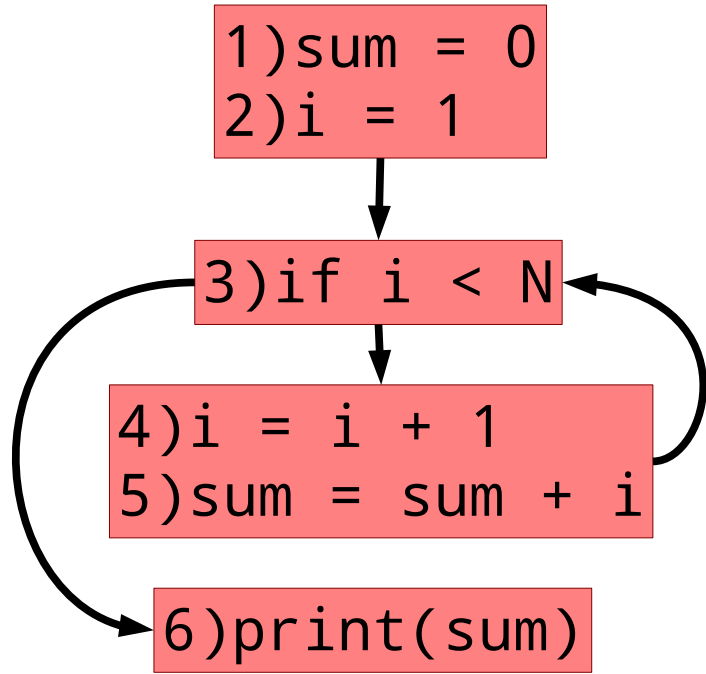
Dynamic Dependence Graph



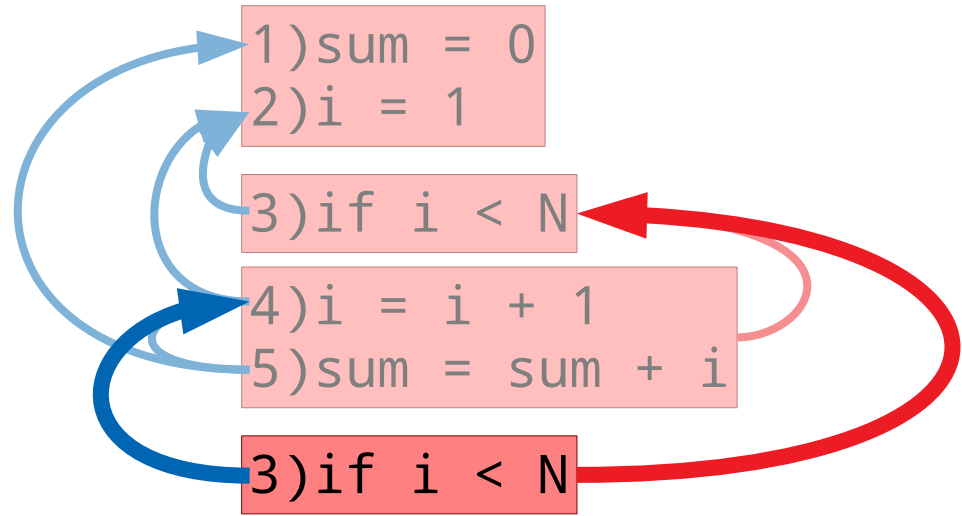
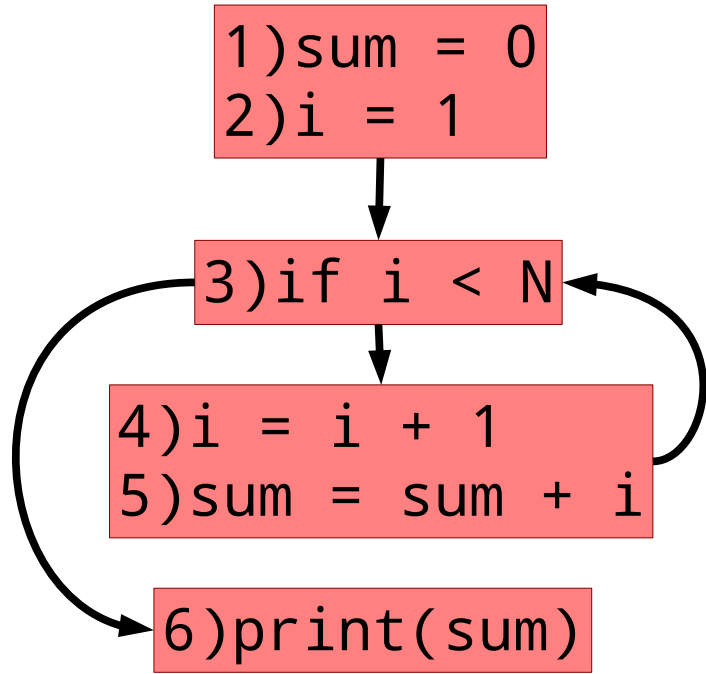
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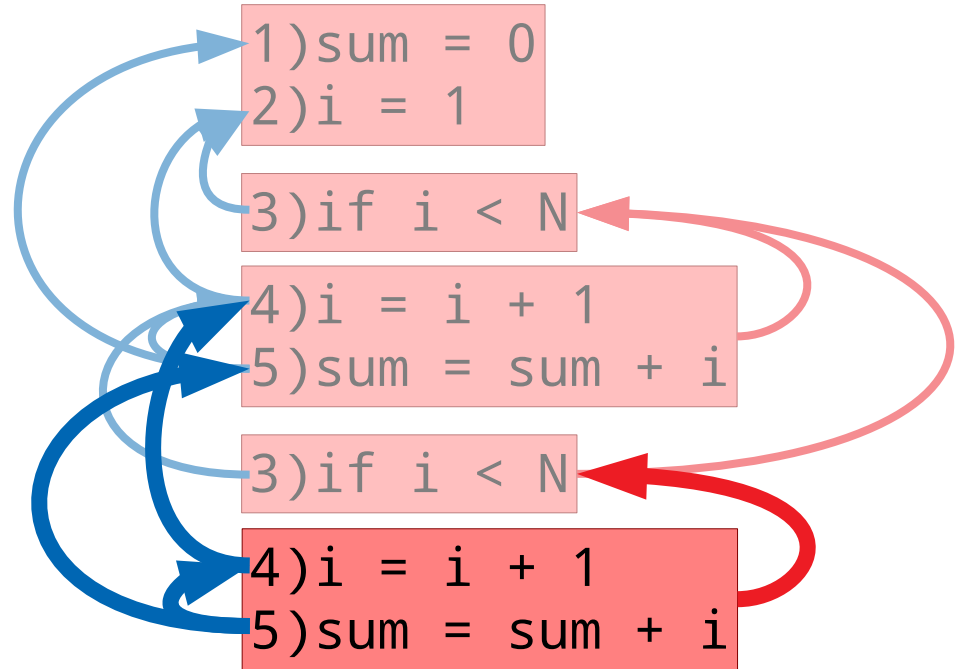
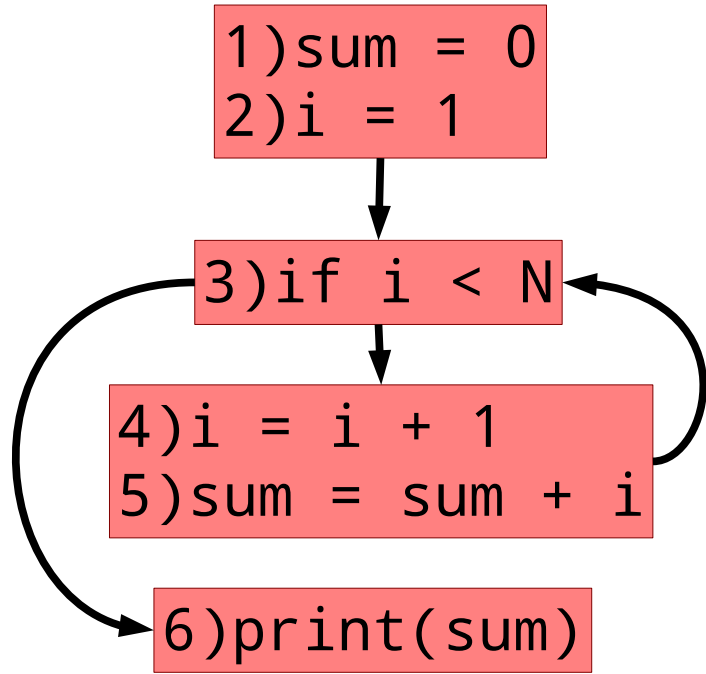
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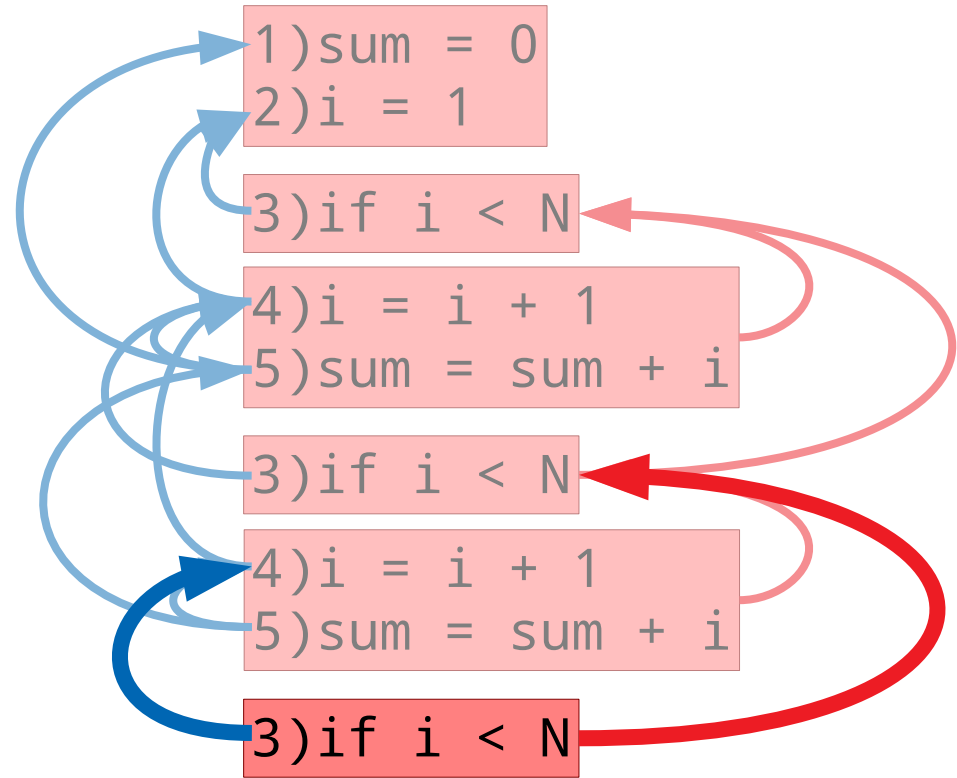
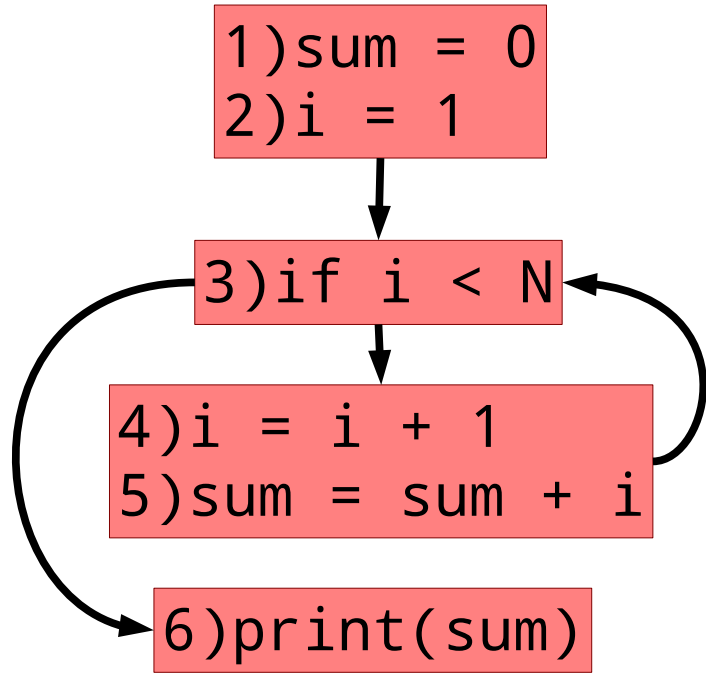
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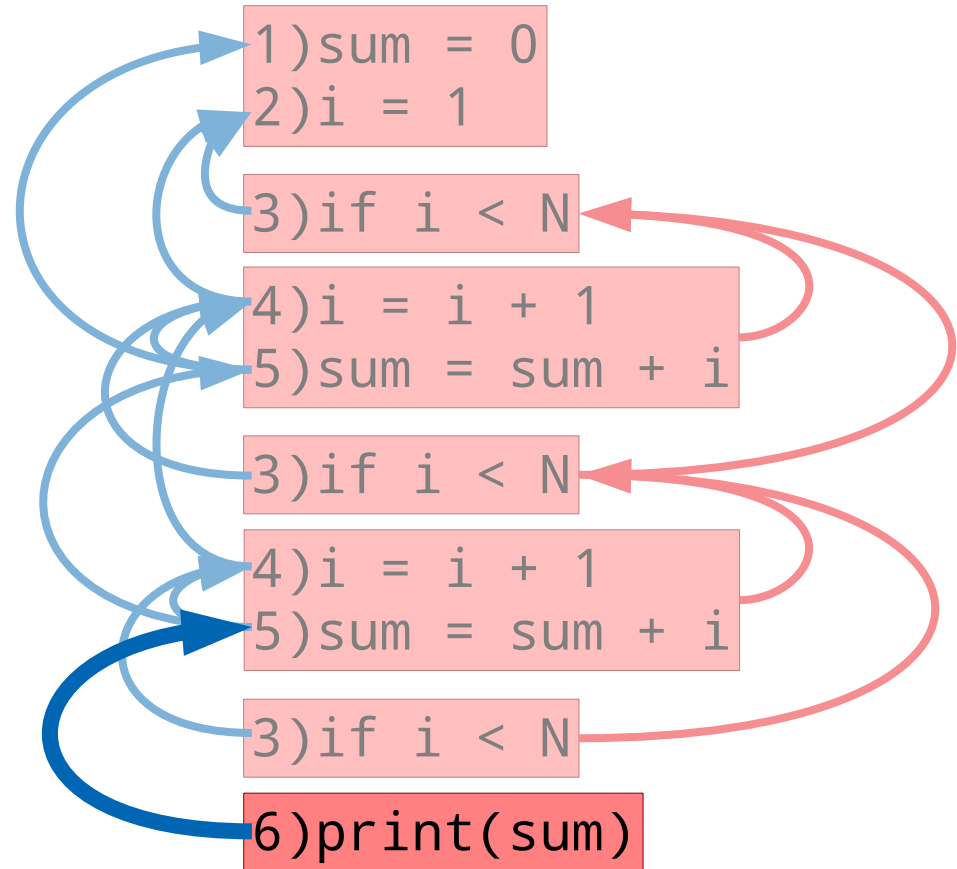
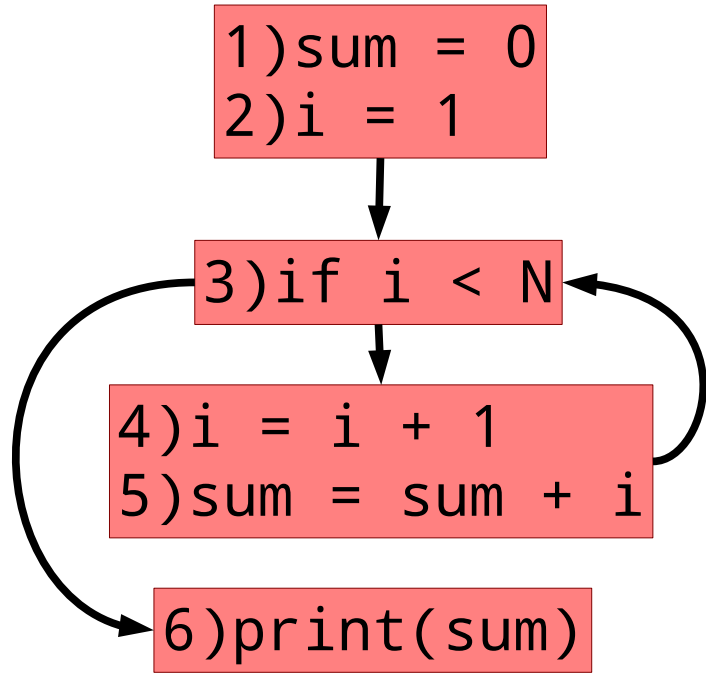
Dynamic Dependence Graph



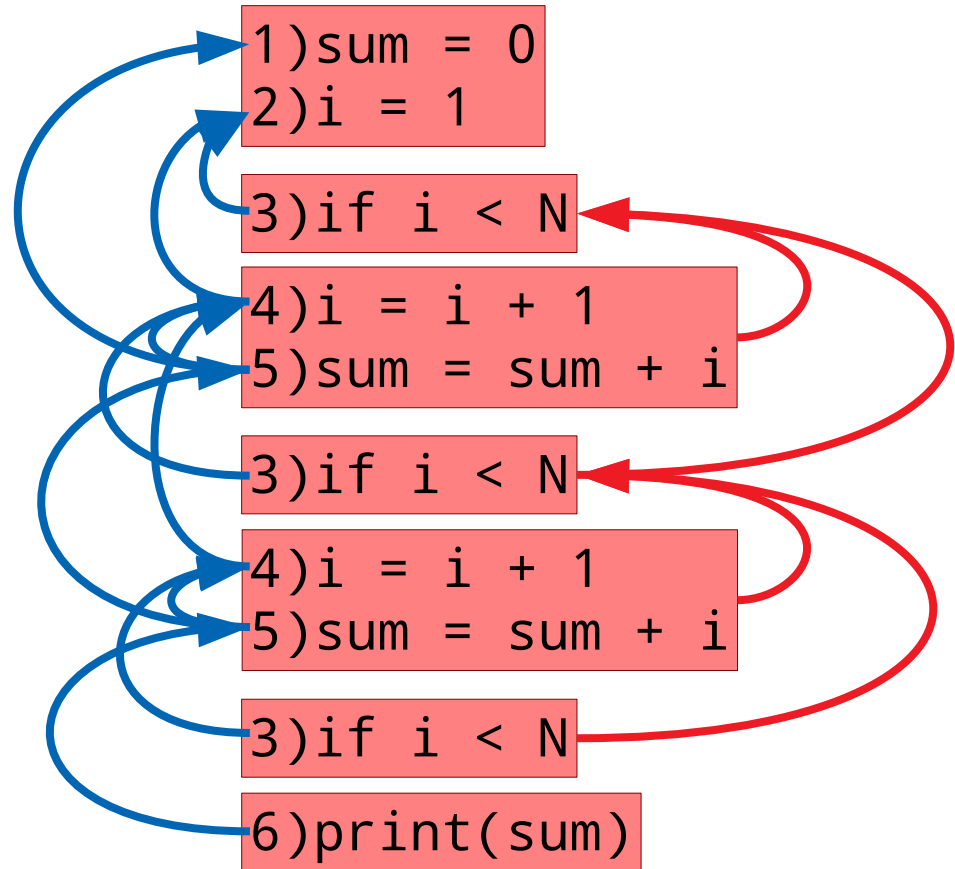
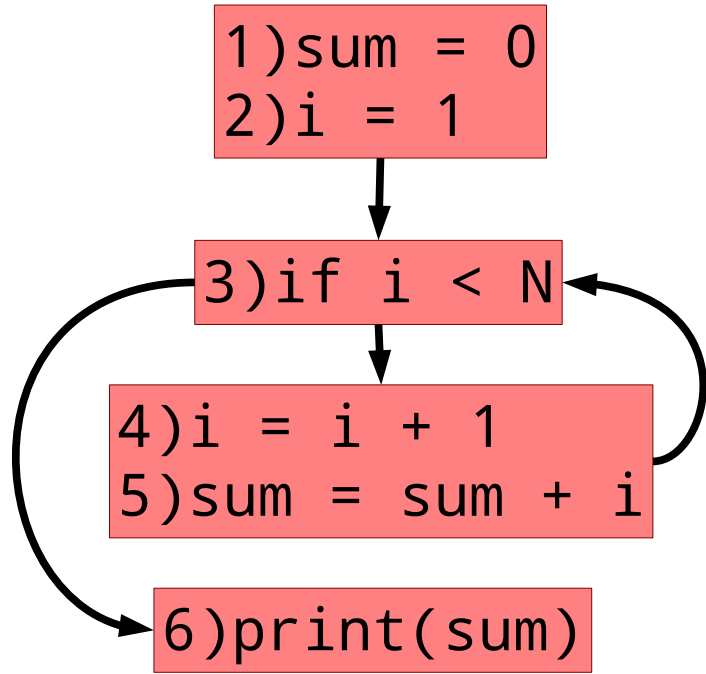
Dynamic Dependence Graph



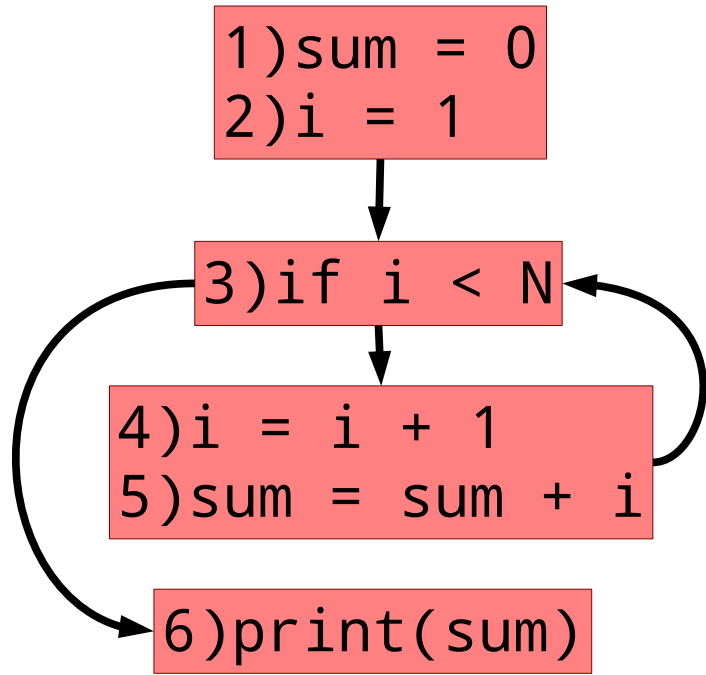
Dynamic Dependence Graph



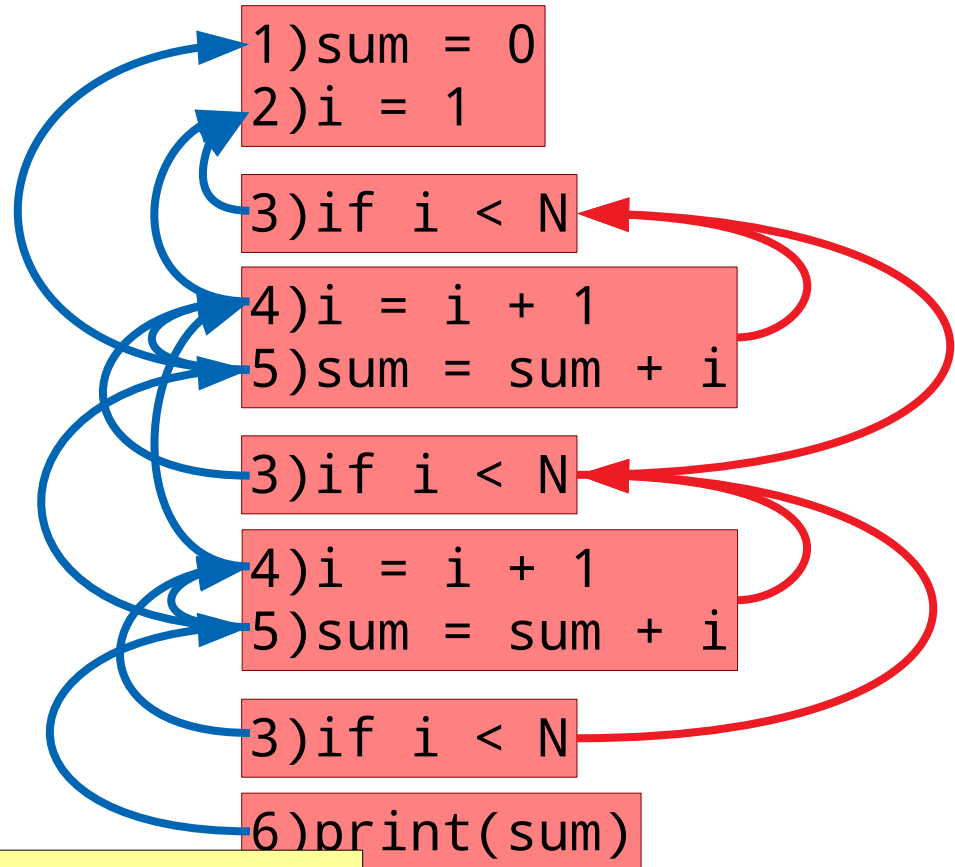
Dynamic Dependence Graph



Dynamic Dependence Graph

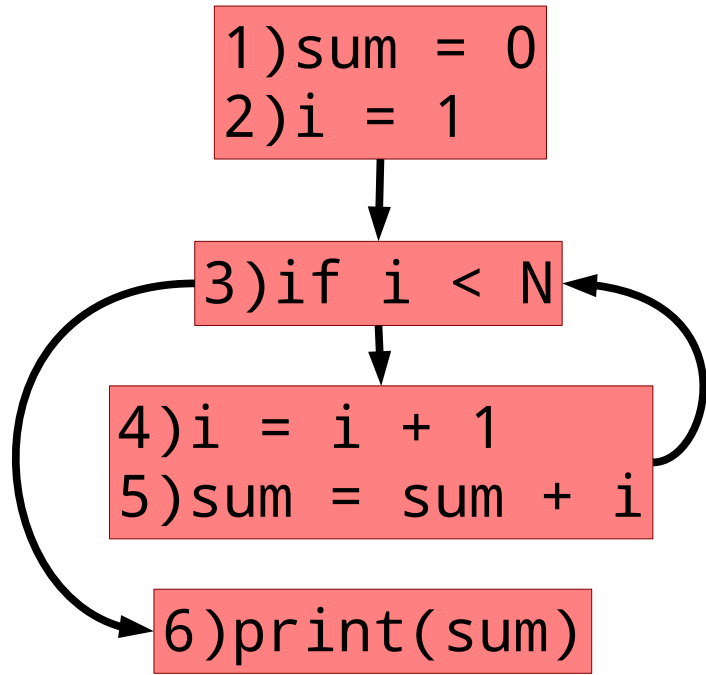


Notably a *bit* difficult for people to wade through.

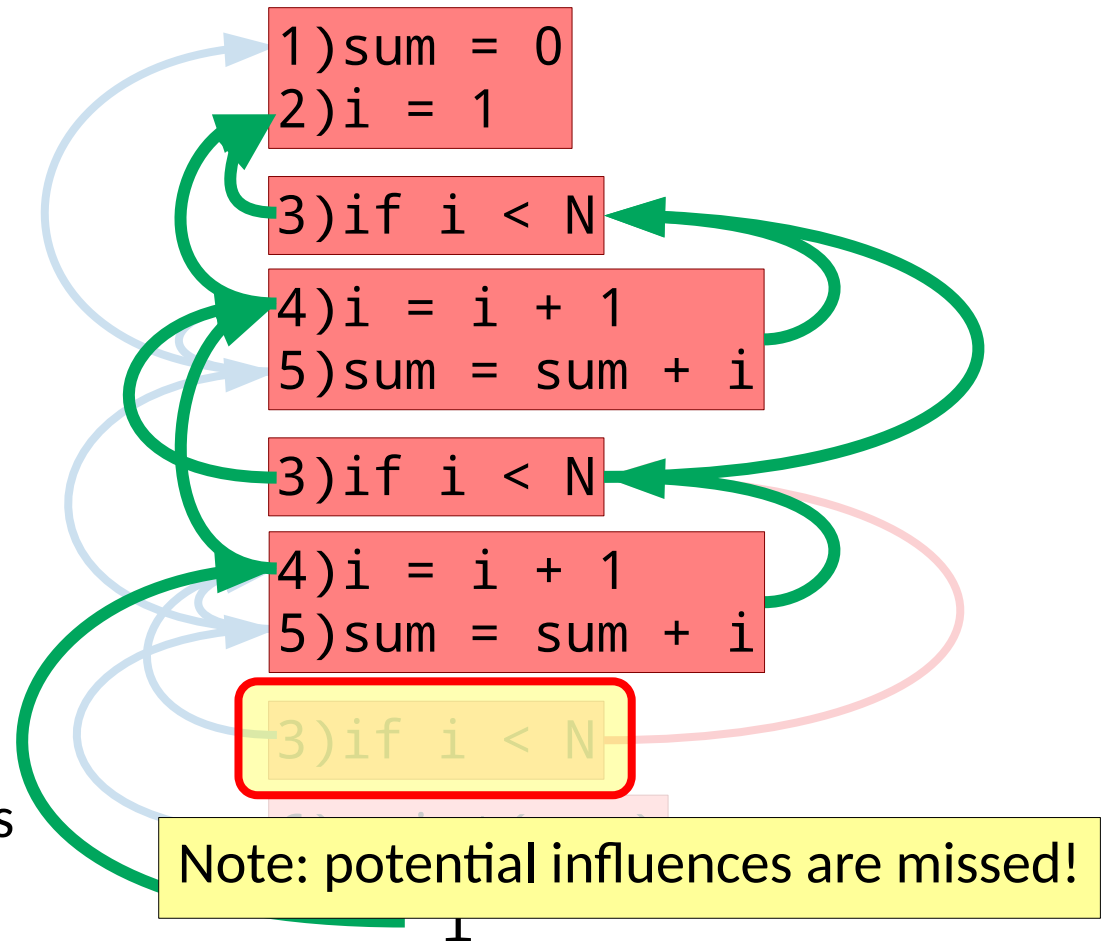


If only we could focus on the parts that interest us...

Dynamic Dependence Graph



Slicing (static or dynamic) computes a transitive closure of dependences



Dynamic Dependence Graphs

Capture a notion of *observed* influence

Dynamic Dependence Graphs

Capture a notion of *observed* influence

- **Debugging:** What caused a bug?

Dynamic Dependence Graphs

Capture a notion of *observed* influence

- **Debugging:** What caused a bug?
- **Security:** How did sensitive information leak?

Dynamic Dependence Graphs

Capture a notion of *observed* influence

- **Debugging:** What caused a bug?
- **Security:** How did sensitive information leak?
- **Testing:** What tests need to be run based on a change?
- ...

Dynamic Dependence Graphs

Capture a notion of *observed* influence

- **Debugging:** What caused a bug?
- **Security:** How did sensitive information leak?
- **Testing:** What tests need to be run based on a change?
- ...

Prioritizing, pruning, & bundling information is often critical when applying slicing

Summary

- Different tasks may benefit from representing programs in different ways
- Thinking of the right representation for the task you have is important