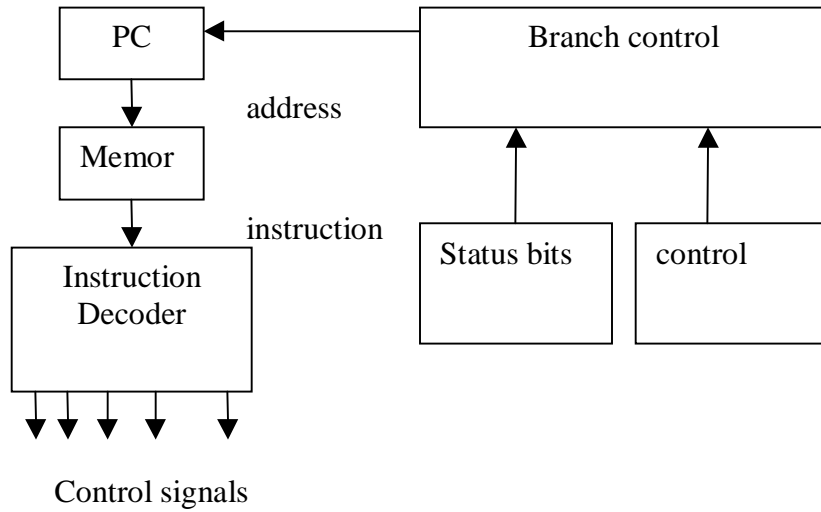


## Back to Control

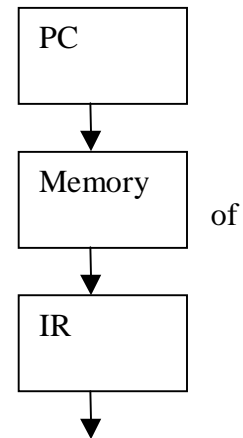


### Control signals:

- Most connect to DP control inputs (FS, MD, AA...)
- Some branch control signals
  - o Branch or not?
  - o Conditional or unconditional
  - o Which signals for conditional branches??
- Control for memory
- Constant-in for DP (for immediate mode instruction eg. Add 7 to R0)
- For single- cycle control the instruction decoder is combinational
- So is the branch control

### Multiple cycle control

- What if we want to spend several cycles on an instruction?
- We need to:
  - o Store the instruction in a register
  - o Decide what to do in each cycle
  - o Store information about the current state (in cycle x instruction y)
- First store the instruction:
  - o IR: the instruction register
  - o Load the IR to do a fetch



## Microprogramming

- We will specify the series of steps to take to complete an instruction with a “micro-program”
  - o A micro-program is part of the CPU design
  - o The program (ie person writing machine code) cannot change it
- Each micro-program instruction (microinstruction)
  - o Each microinstruction will execute in one cycle
  - o It will activate whatever control signals are needed to do this job
- In our example architecture there will be a total of 256 microinstructions
  - o These will have to implement every instruction the processor can do
  - o The microinstruction will consist of a string of control bits.
    - They aren't decoded, just sent to the appropriate control signals
  - o Microinstruction will be stored in a ROM

## Micro-control

- We will use the same instruction format as the single-cycle CPU
  - o 7 bit op-code
  - o 3x3bit operands
- the first microinstruction to do when executing instruction X will be 0||X
  - o ie instruction → put a 0 on the front = control address
- control address = location in control memory
- programmer wants op-code 1010101 executed
  - o put a 0 on the front
    - 01010101
  - o that's the control address
    - input to control memory
  - o output of the control memory is the micro instruction
    - control signals sent to start the work

