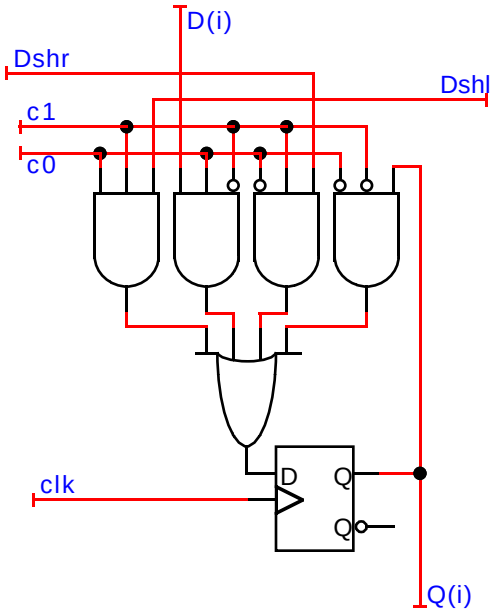
Universal Shift Register Bitslice



4-bit Universal Shift Register

