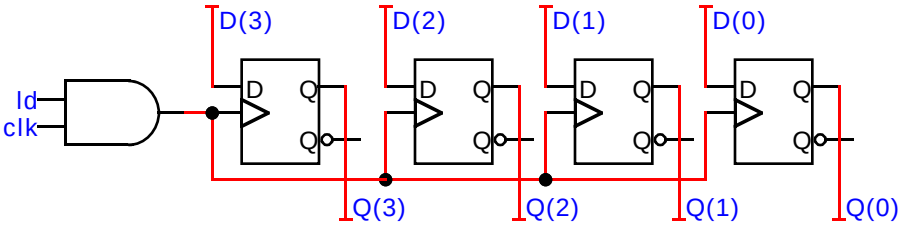
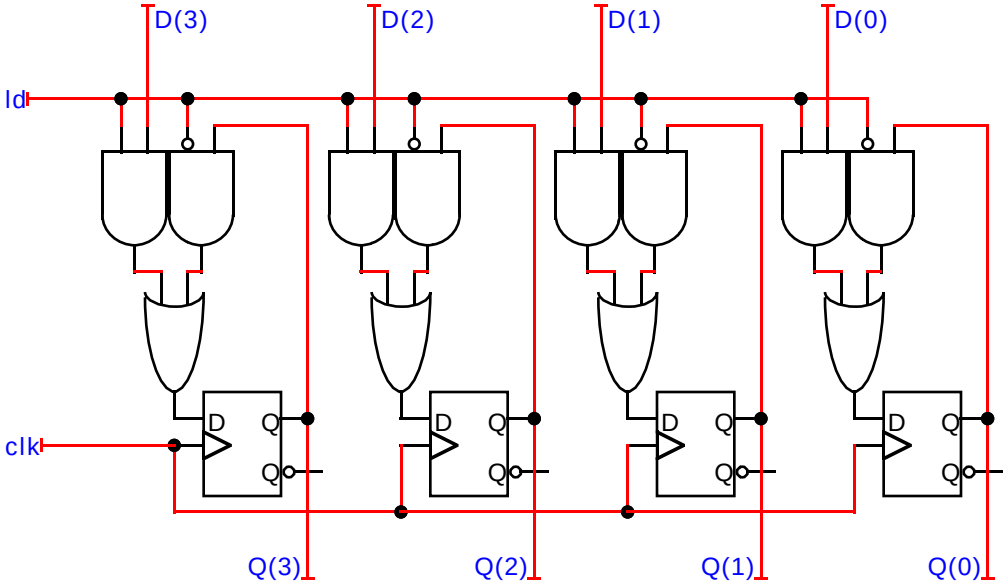
Load/Hold Register - Clock Gating



Load/Hold Register - Input Logic



Part Symbol for Both

